

Fig. 1

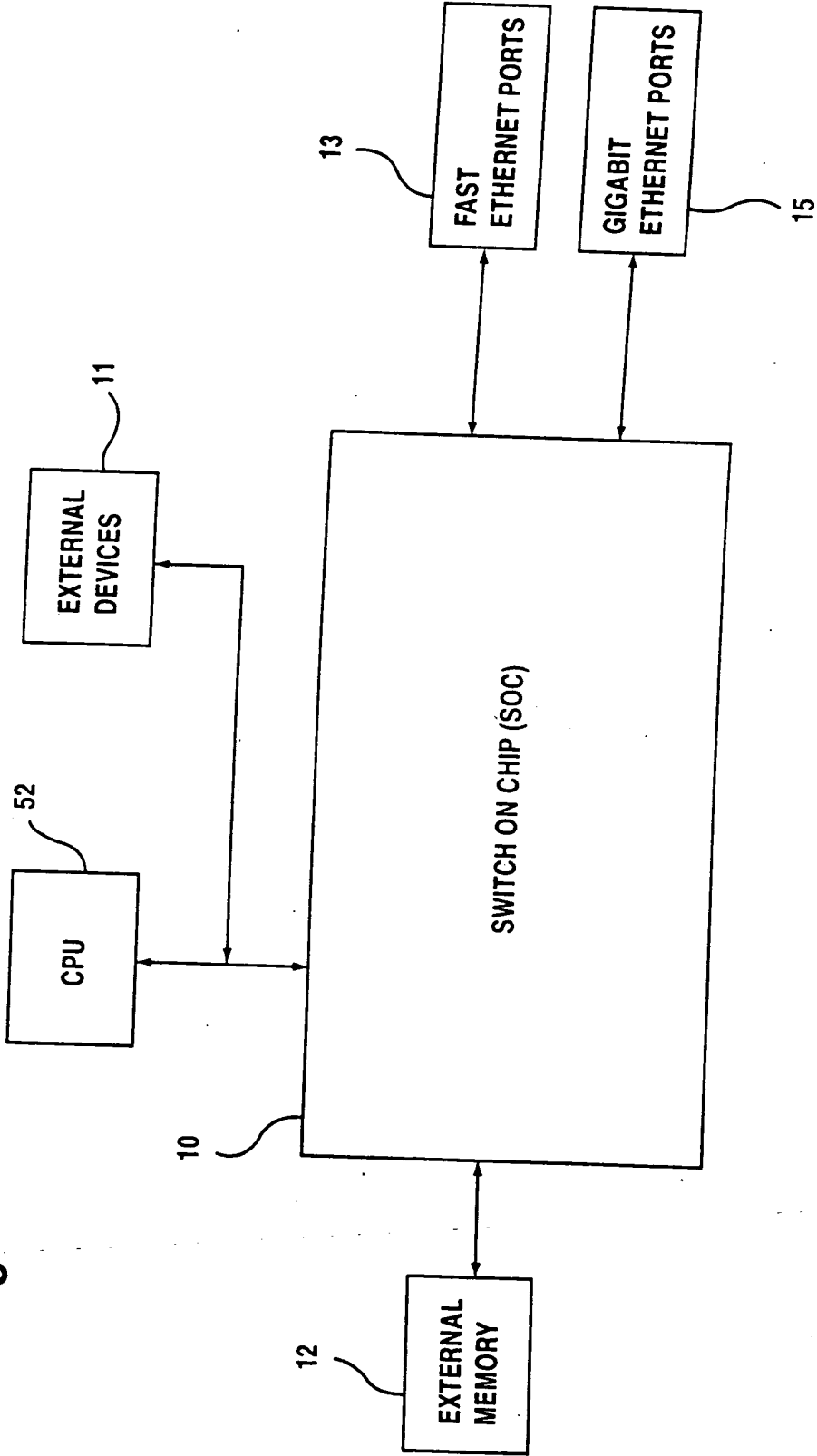


Fig.2

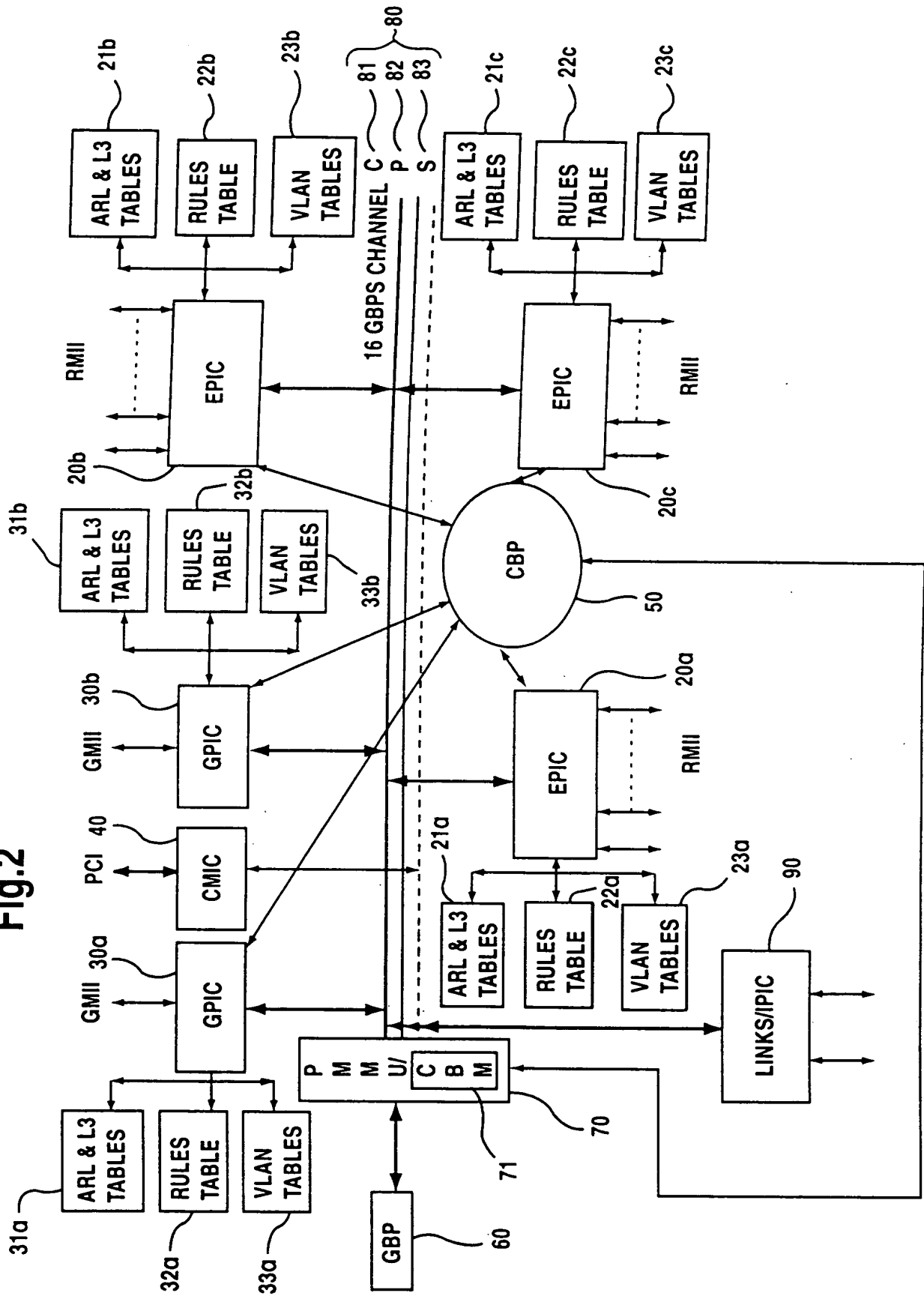
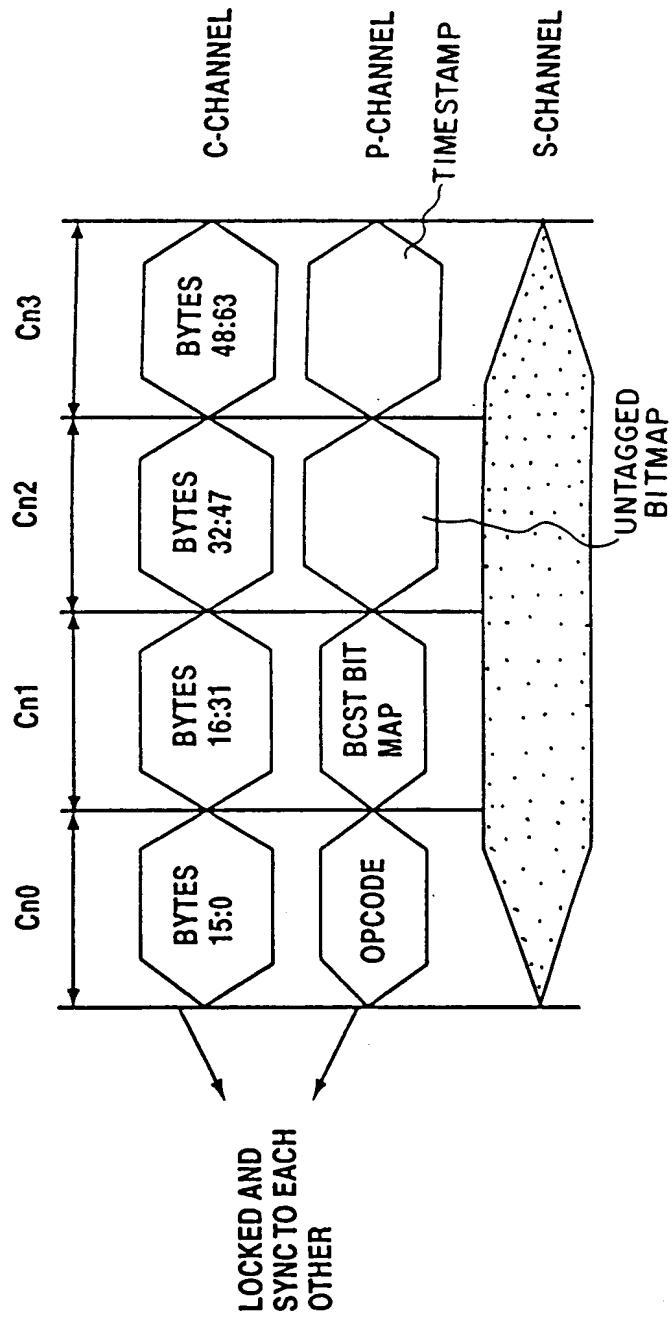


Fig.3



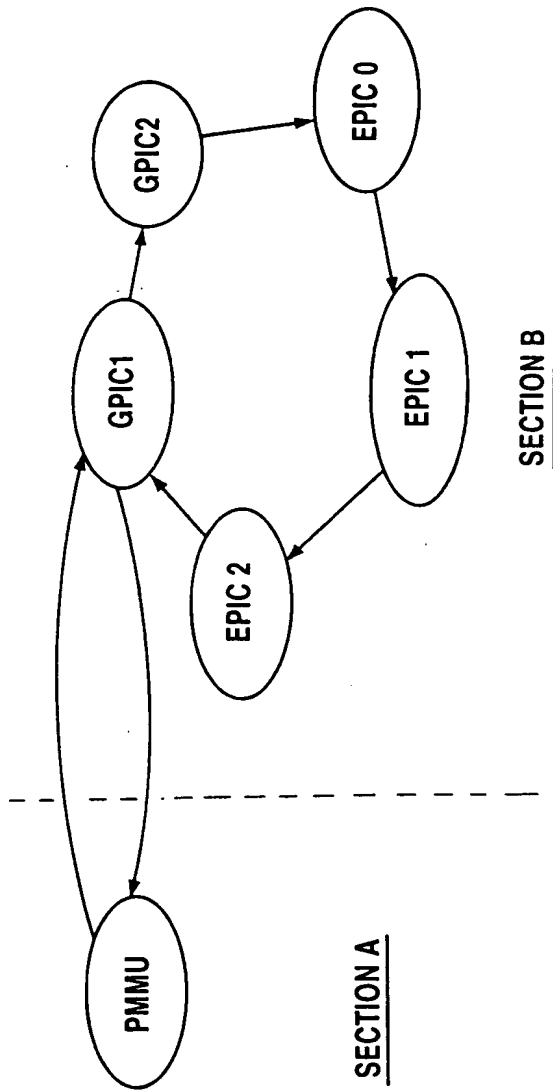


Fig.4a

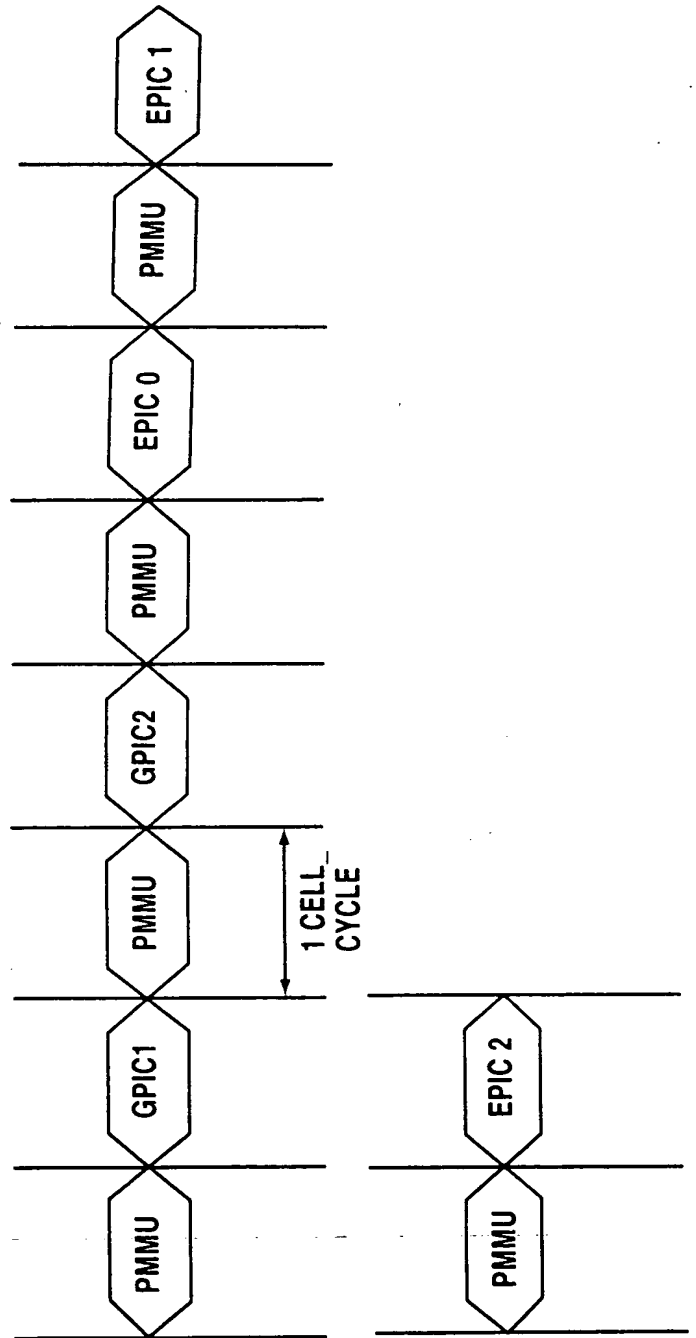


Fig.4b

Fig.5

PROTOCOL CHANNEL MESSAGES

| | | | | | | | | | | | | | | | |
|------------|----|----------|-------------|---------------|----|----|----|-----|----|----|---|---------|---|---|-----|
| 30 | 28 | 26 | 24 | 22 | 20 | 18 | 16 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 |
| OP CODE | I | RESERVED | NXT CELL | SRC DEST PORT | | | | COS | J | S | E | CR C | P | O | LEN |
| | P | | | | | | | | | | | | | | |
| | X | | | | | | | | | | | | | | |

| | | | | | | | | | | | | | | | |
|----------|----|------------------|----|----|----|----|----|----|----|----|---|---|---|---|---|
| 30 | 28 | 26 | 24 | 22 | 20 | 18 | 16 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 |
| RESERVED | | BC/MC PORTBITMAP | | | | | | | | | | | | | |

| | | | | | | | | | | | | | | | |
|----|-----|--|----|----|----|----|----|----|----|----|---|---|---|---|---|
| 30 | 28 | 26 | 24 | 22 | 20 | 18 | 16 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 |
| U | RES | UNTAGGED PORTBITMAP/SRC PORT NUMBER (BIT0.5) | | | | | | | | | | | | | |

| | | | | | | | | | | | | | | | |
|-------------|----|----|----|----|----|----|----|----|----|------------|---|---|---|---|---|
| 30 | 28 | 26 | 24 | 22 | 20 | 18 | 16 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 |
| CPU OPCODES | | | | | | | | | | TIME STAMP | | | | | |

SIDE BAND CHANNEL MESSAGES

DATA

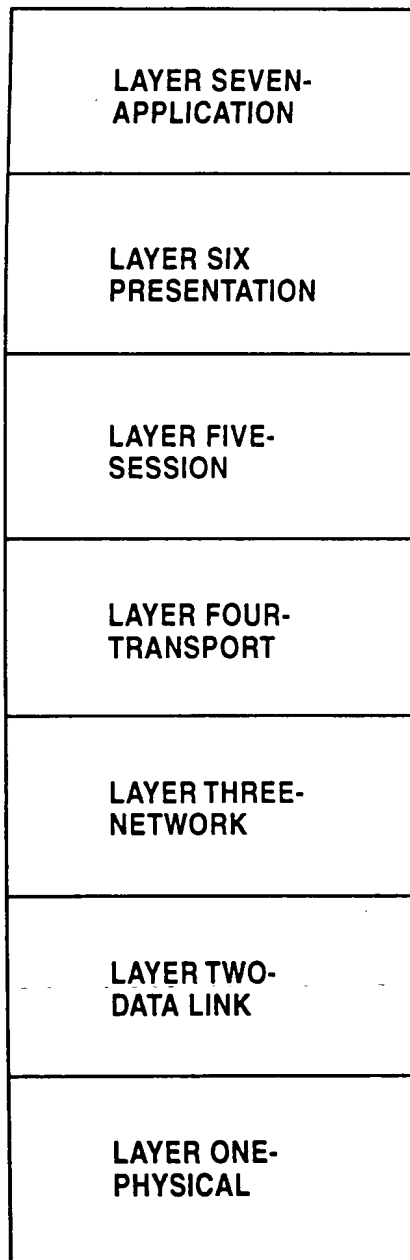


Fig. 8

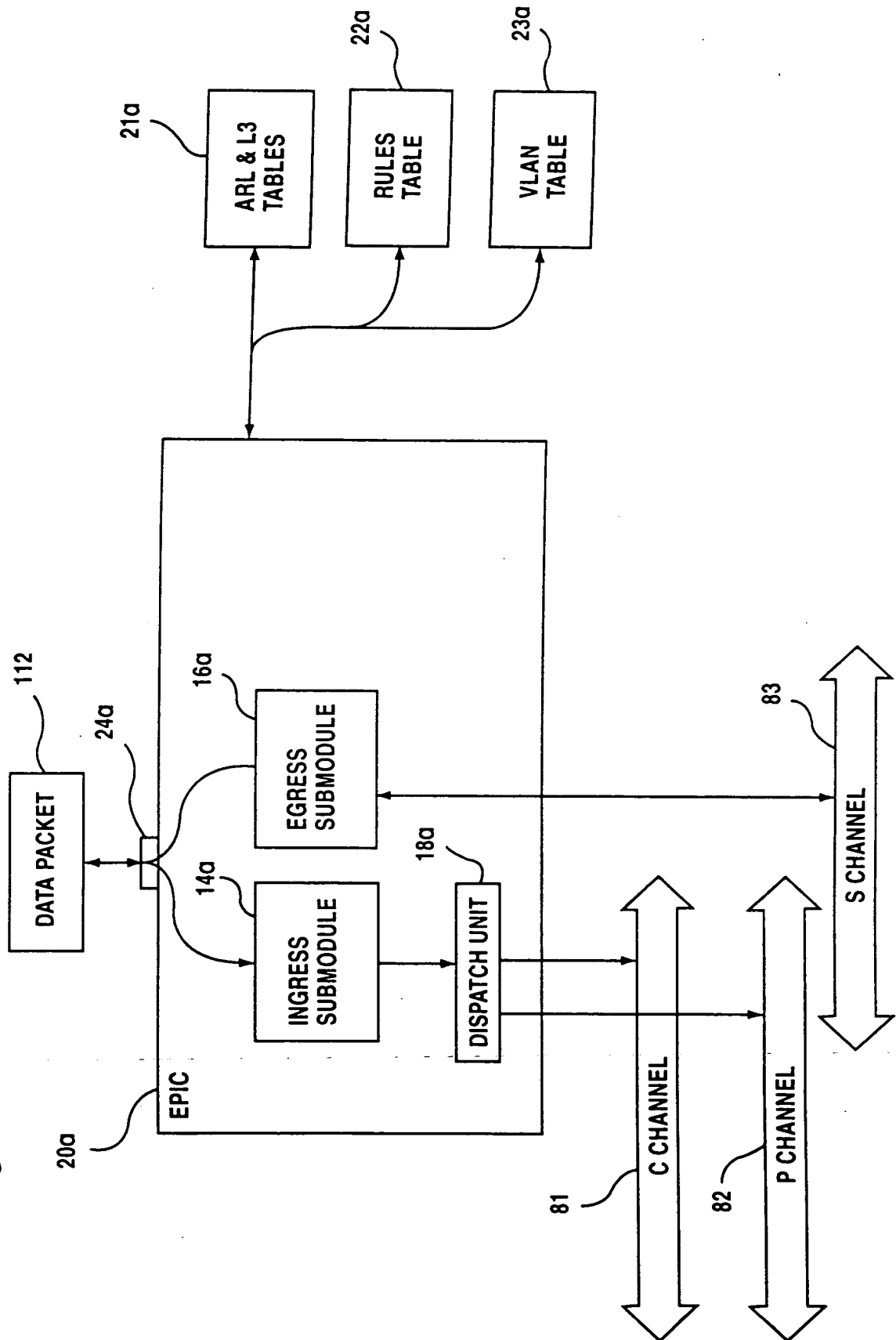


Fig.9

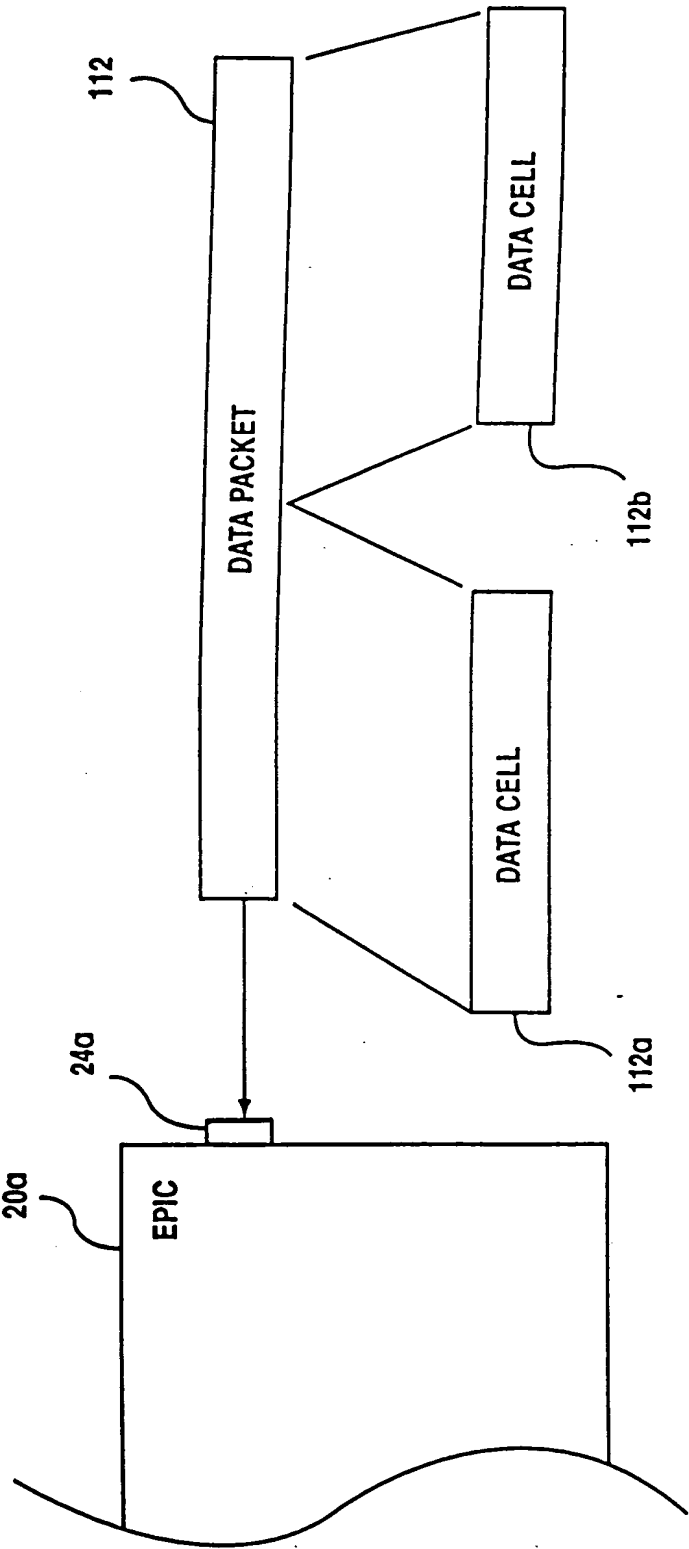


Fig.10

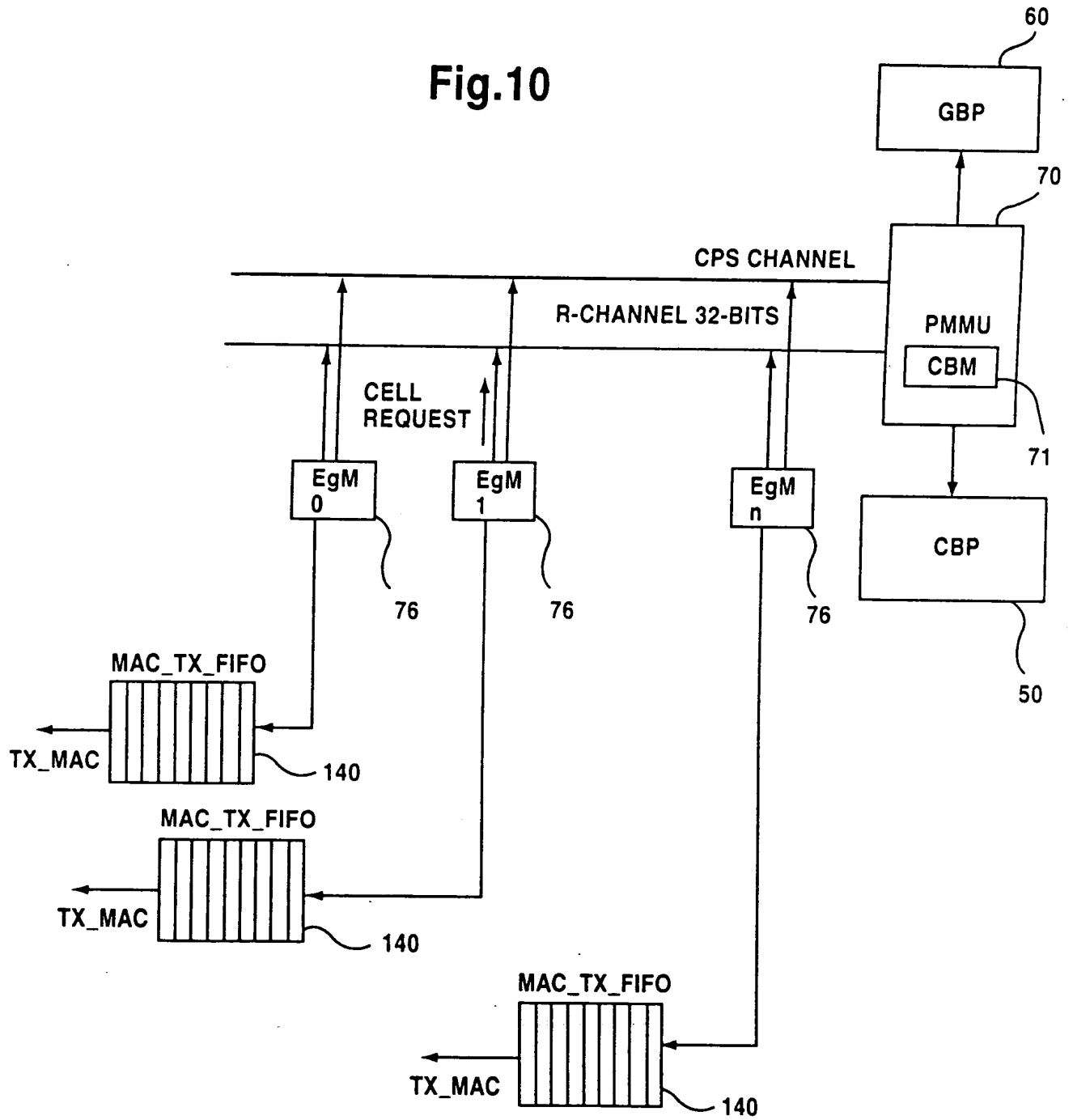


Fig.11

| | |
|----------|---|
| LINE 0 → | FC LC BC/MC CPY_CNT(5b) CELL_LENGTH(7b) CRC(2b) NC_HEADER(16b) SRC_COUNT(6) IPX IP TIME_STAMP(14b) O BITS(2b) P NEXT CELL LEN(2b) CPU_OPCODE(4b) CELL_DATA(0-9B) |
| LINE 1 → | CELL_DATA(10-27) BYTES |
| LINE 2 → | CELL_DATA(28-45) BYTES |
| LINE 3 → | CELL_DATA(46-63) BYTES |

Fig.12

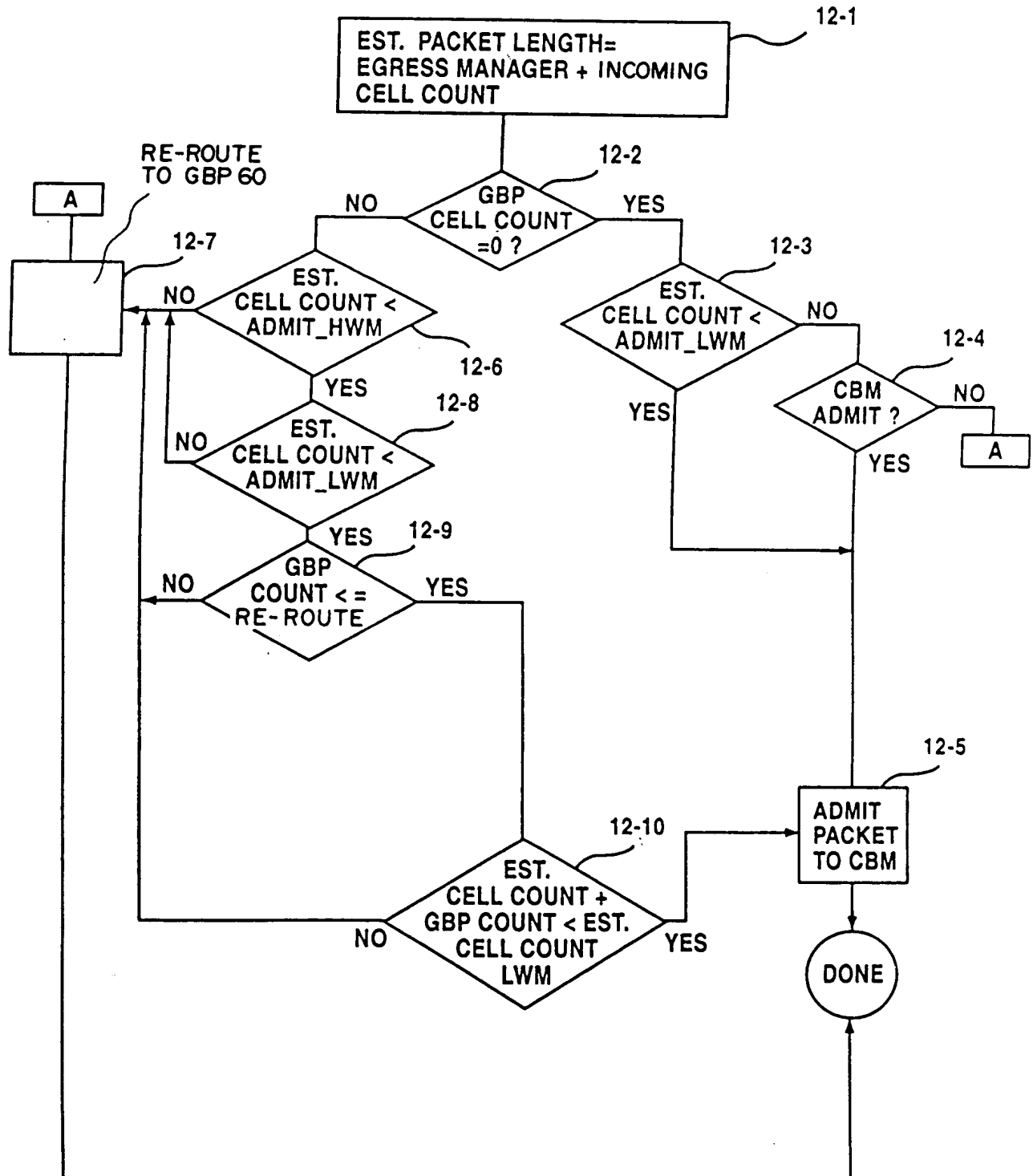


Fig.13

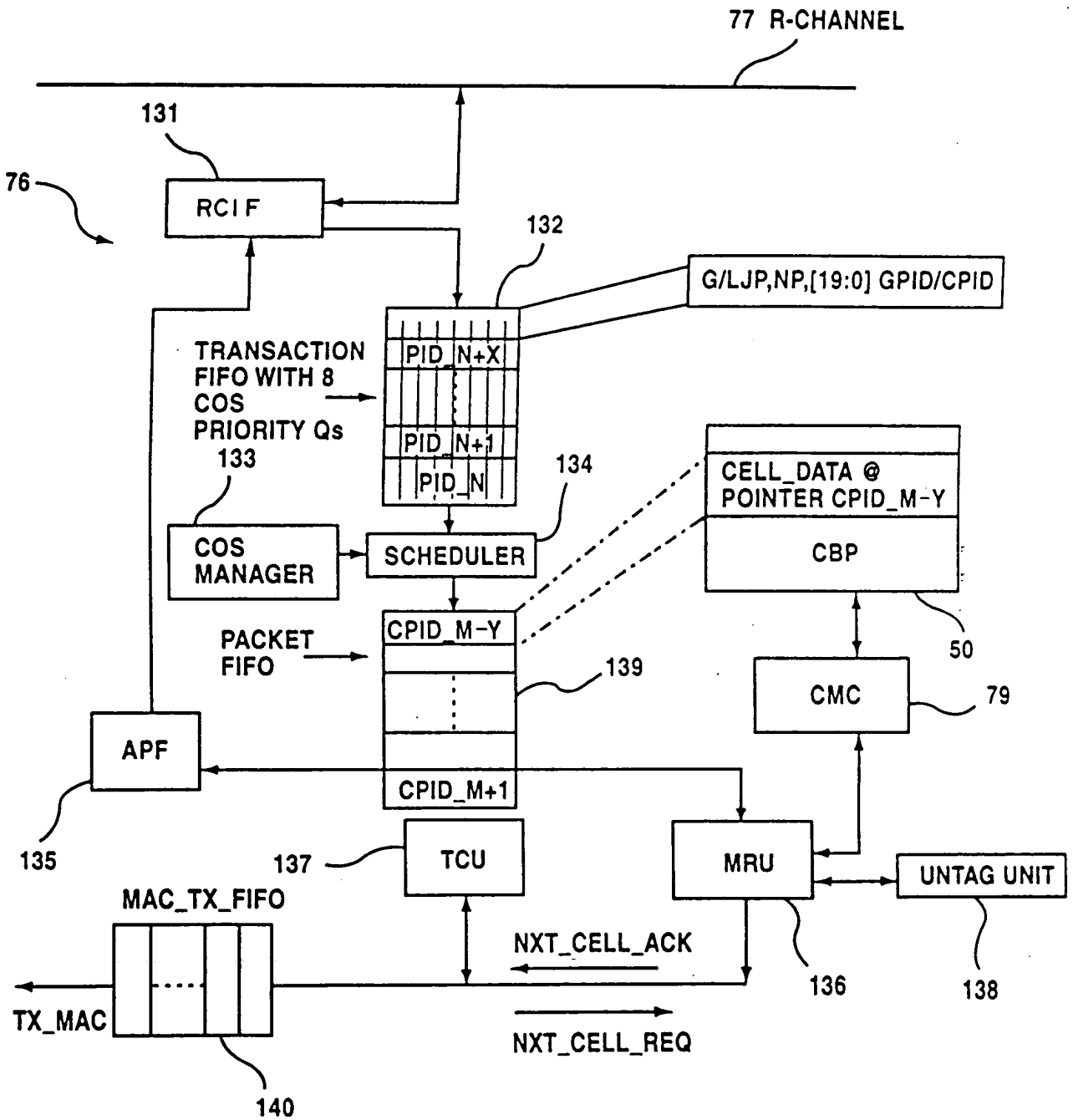
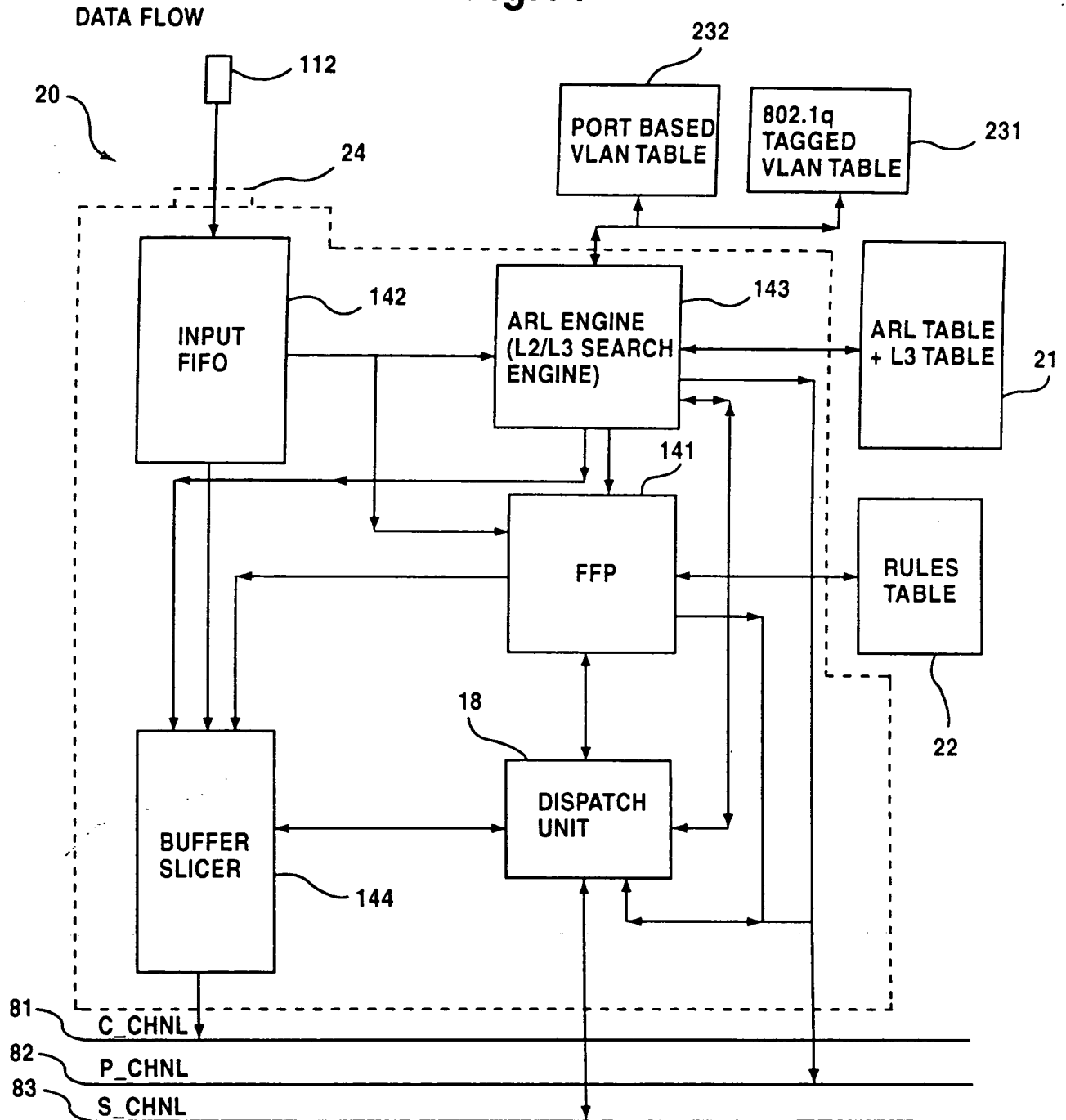


Fig.14



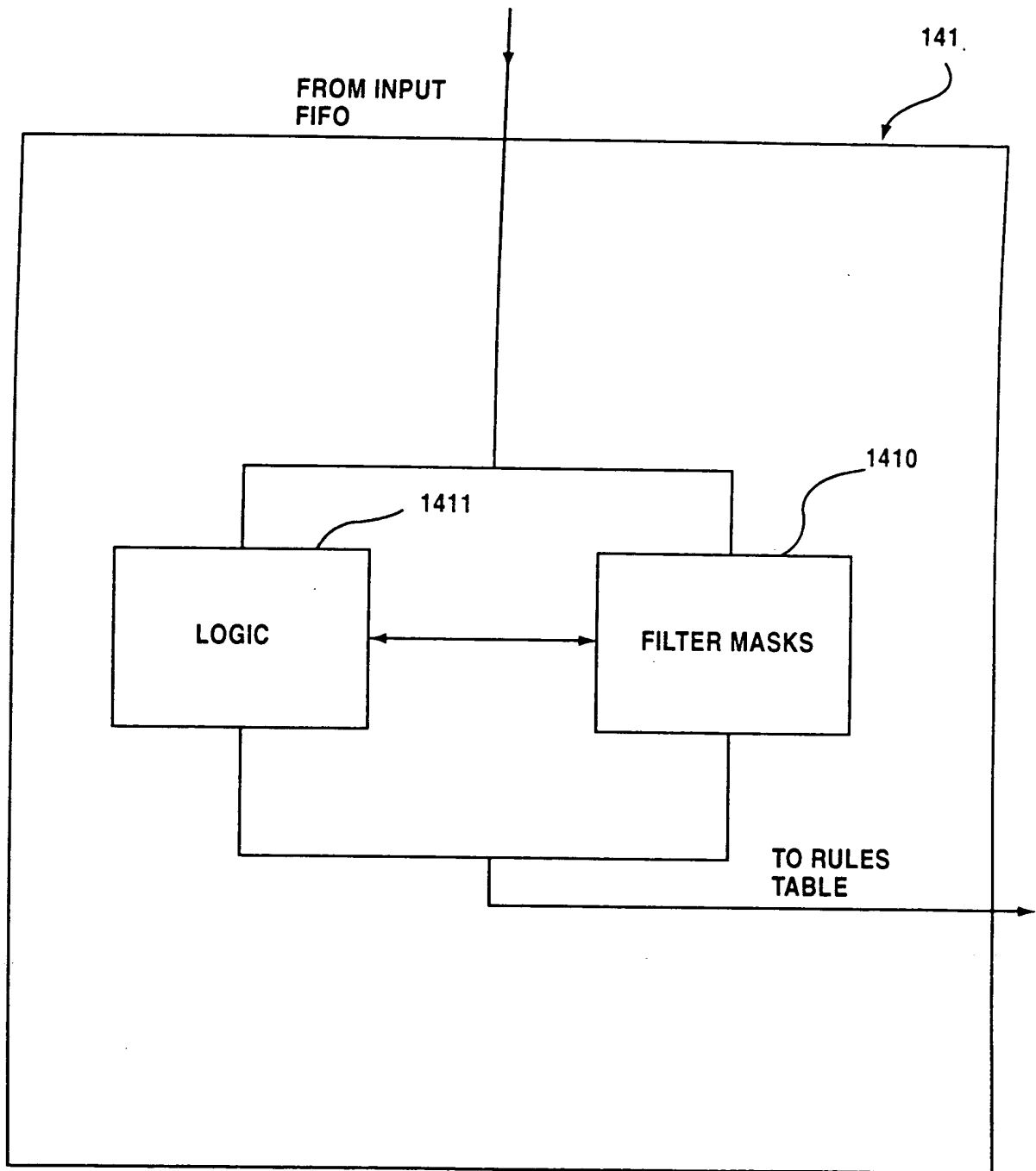


Fig.16

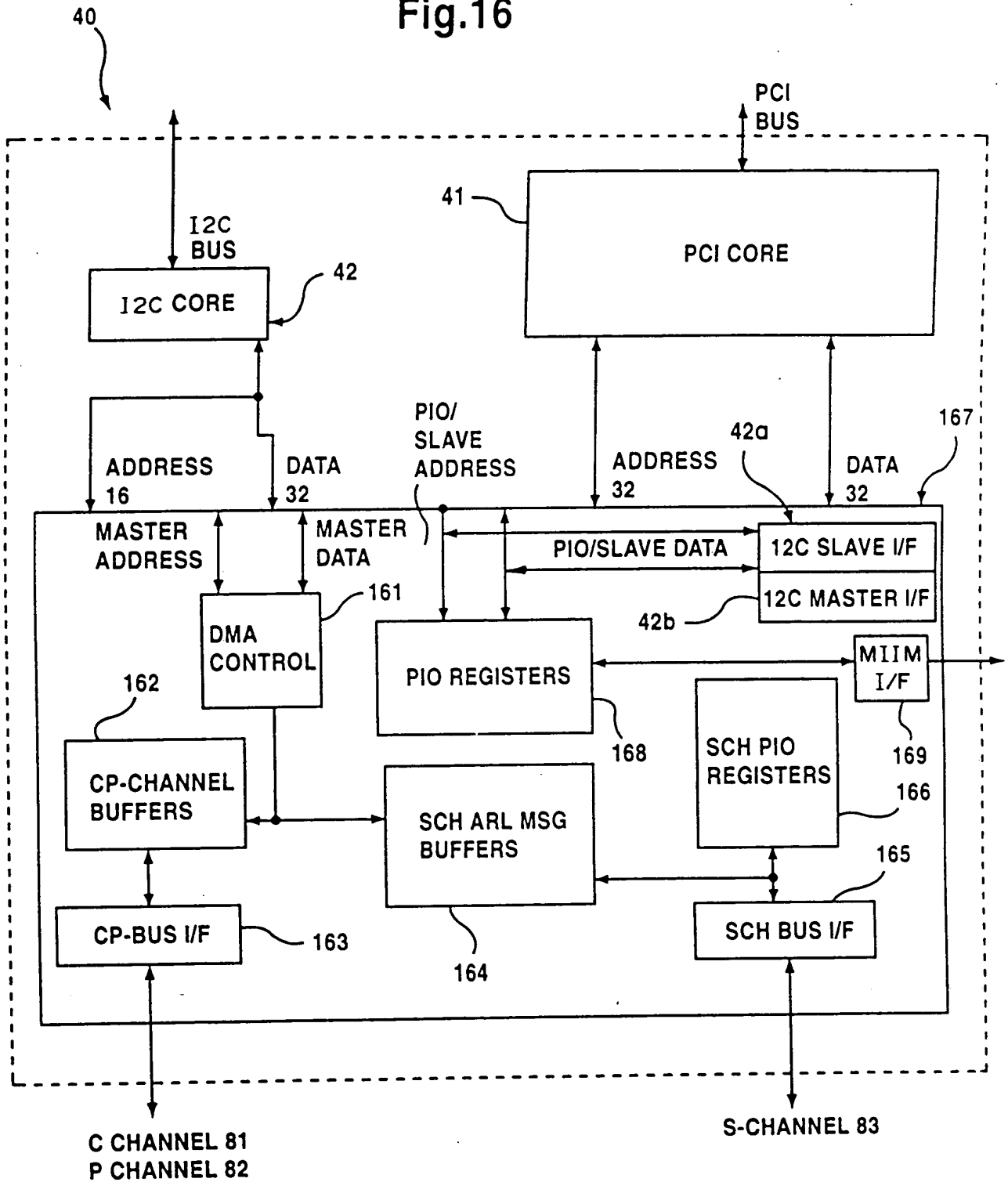


Fig.17

FFP PROGRAMMING FLOW CHART

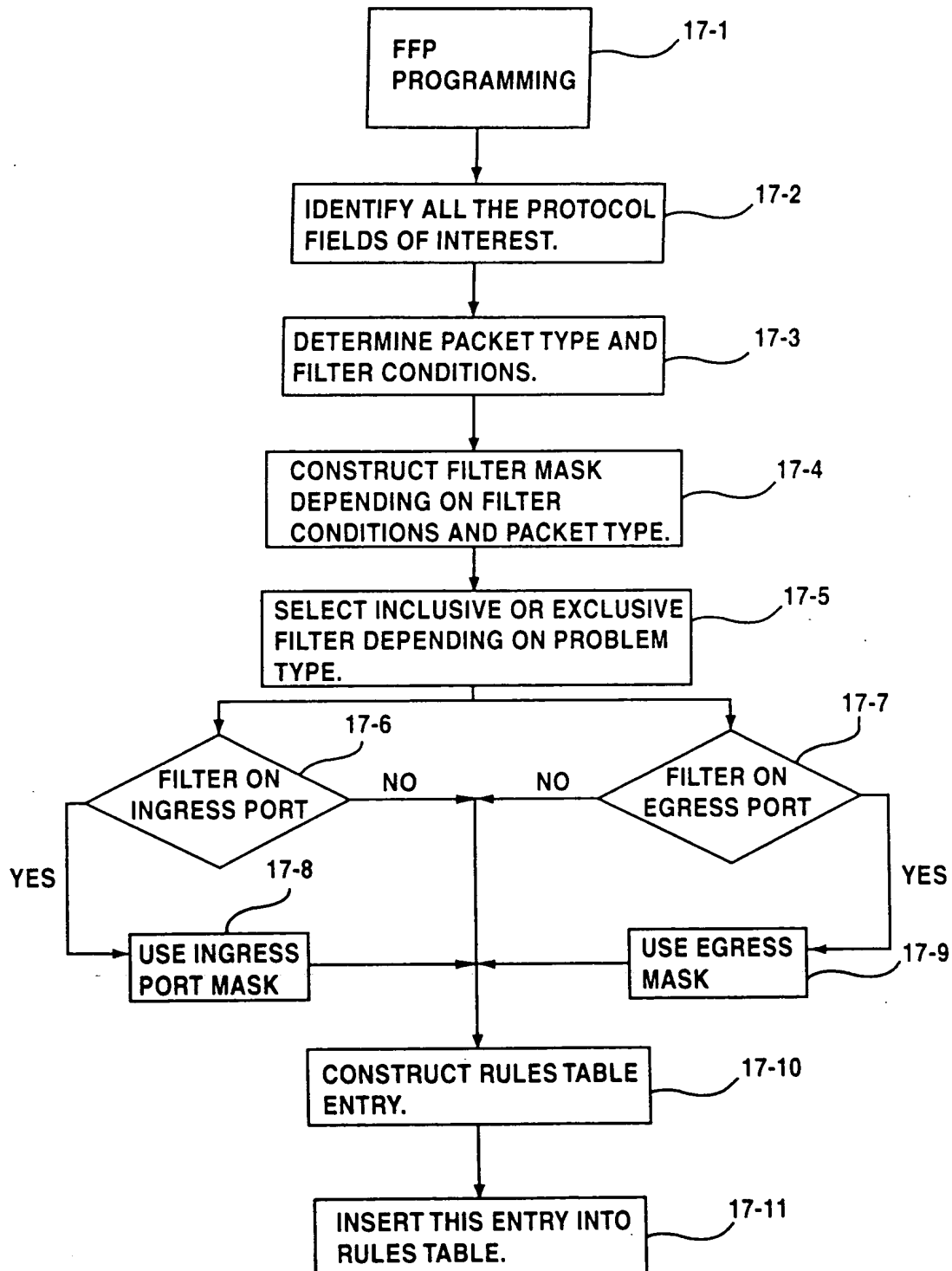


Fig.18

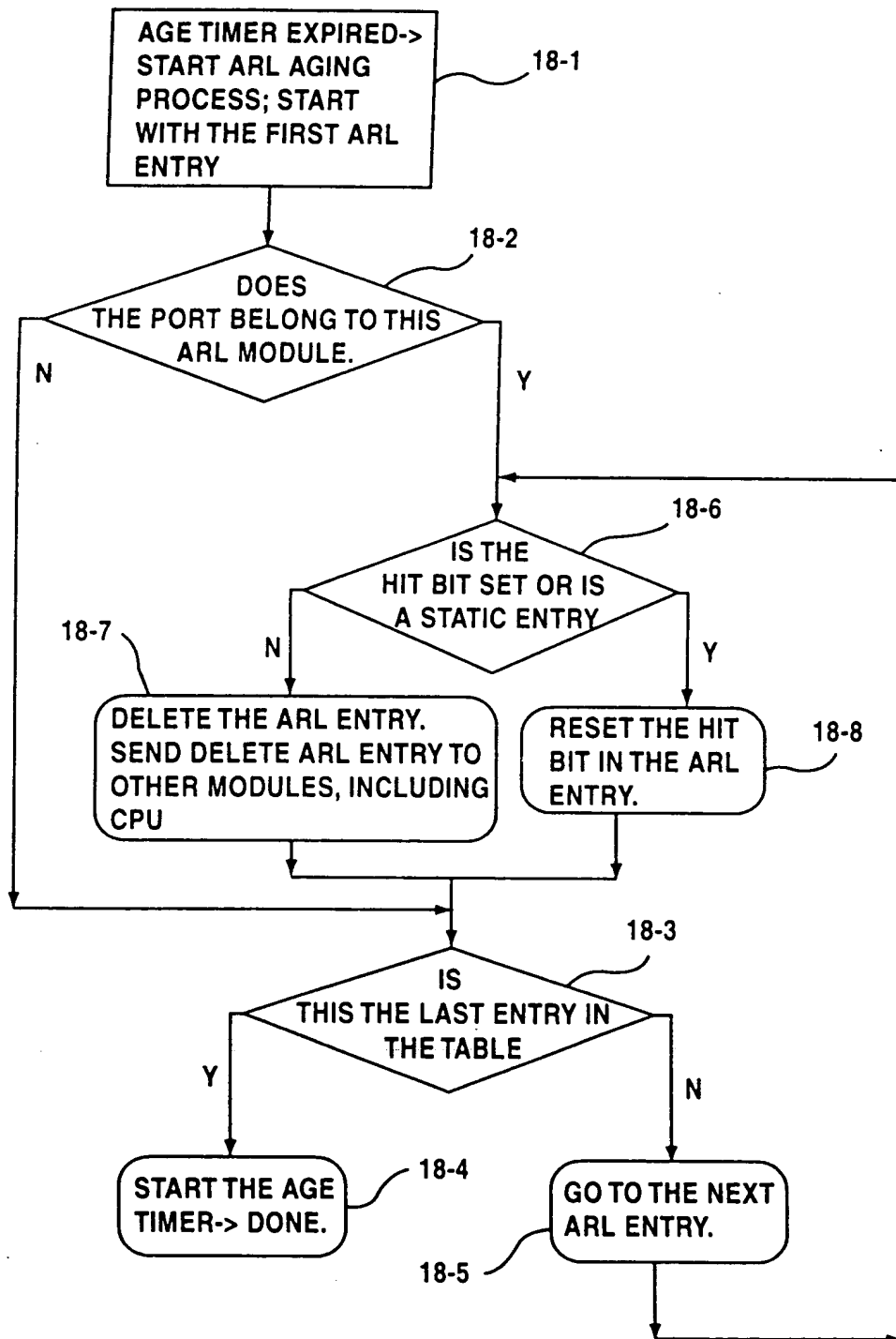
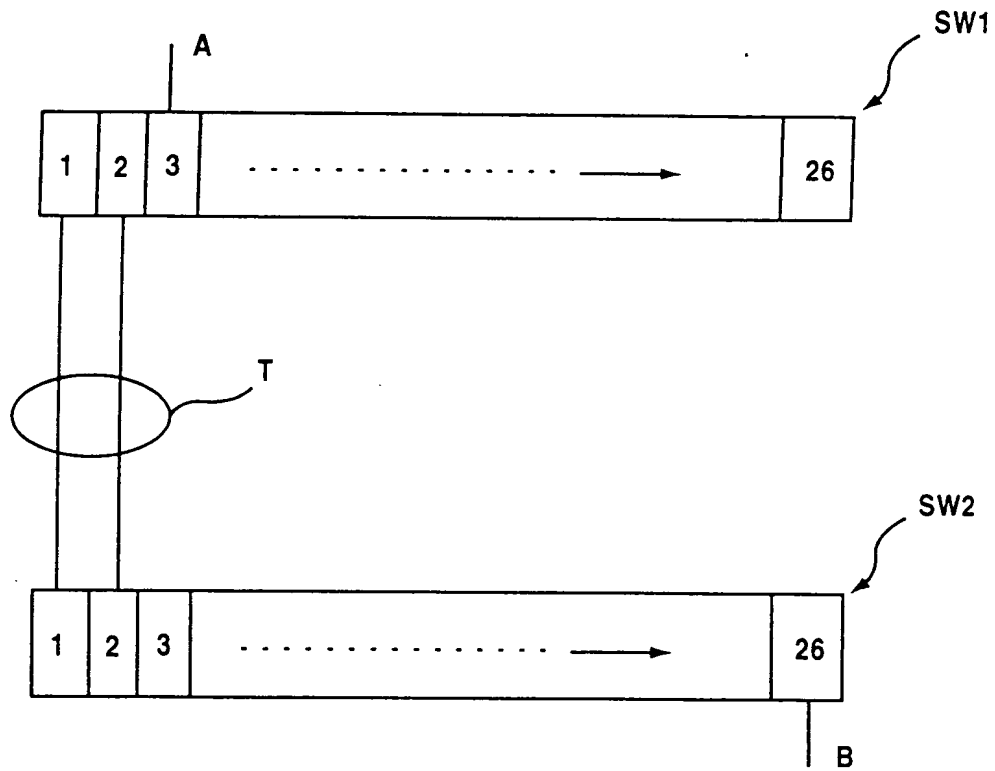
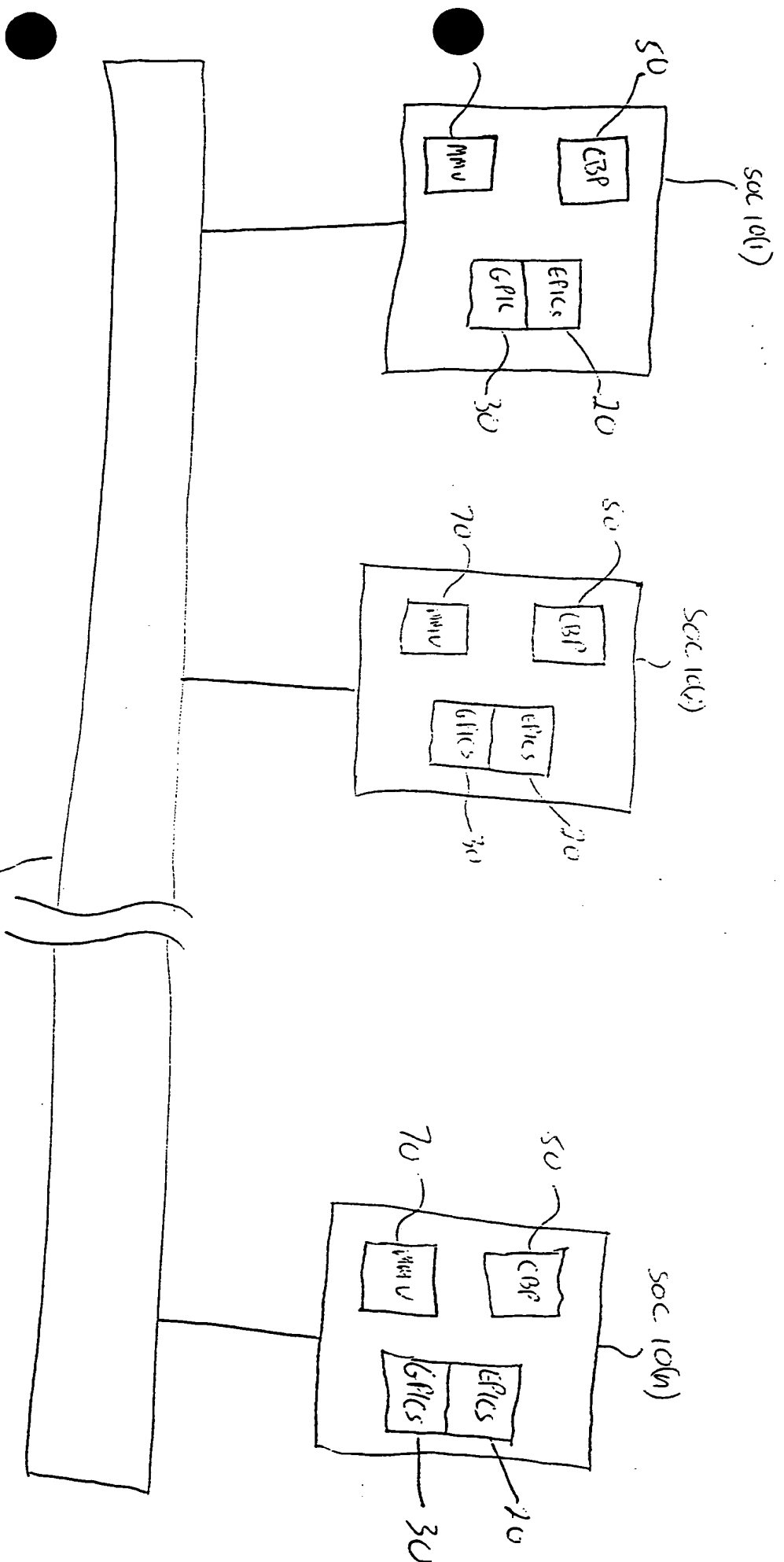


Fig.19





Interstack connection I

Fig 20

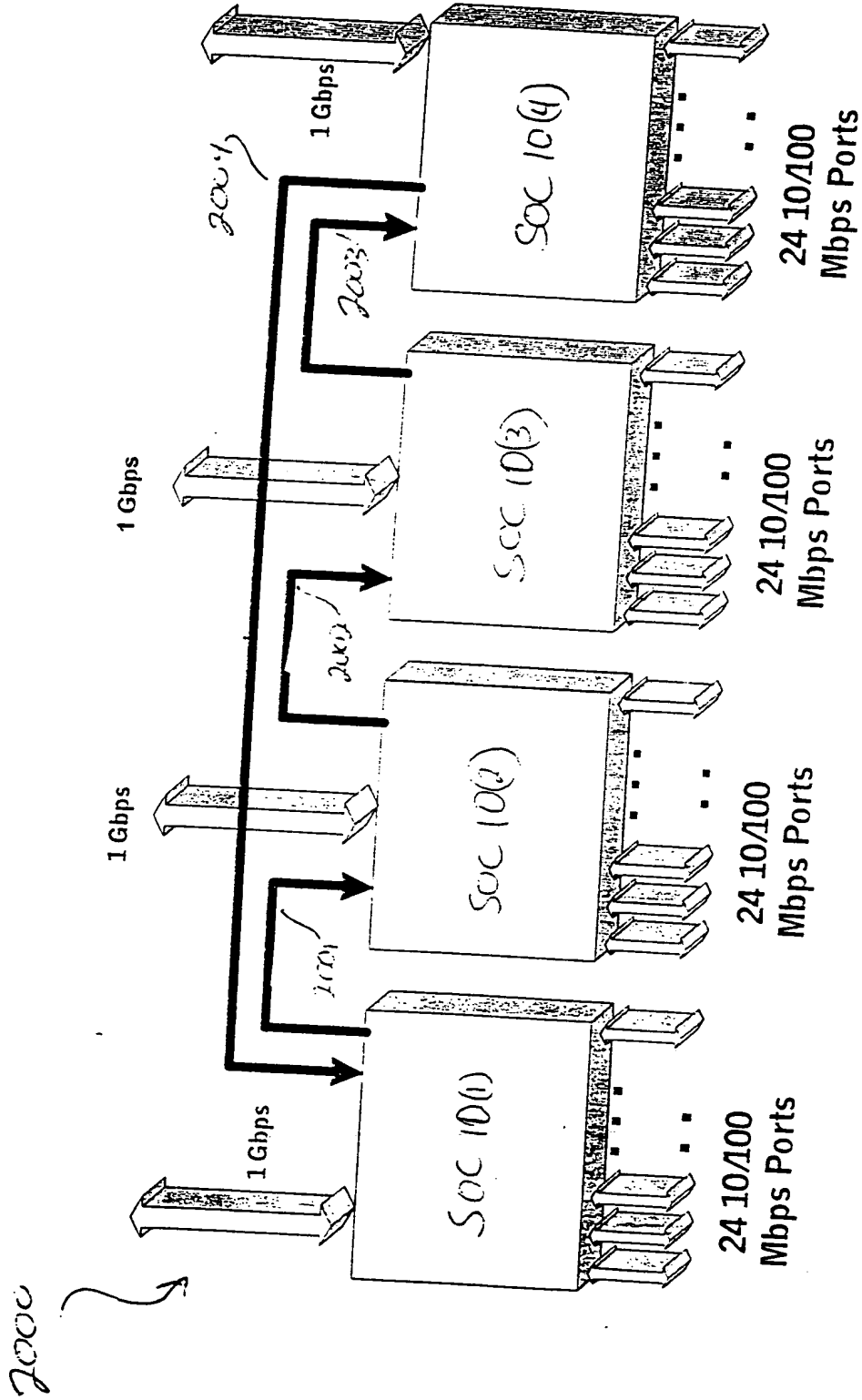


Fig. 21

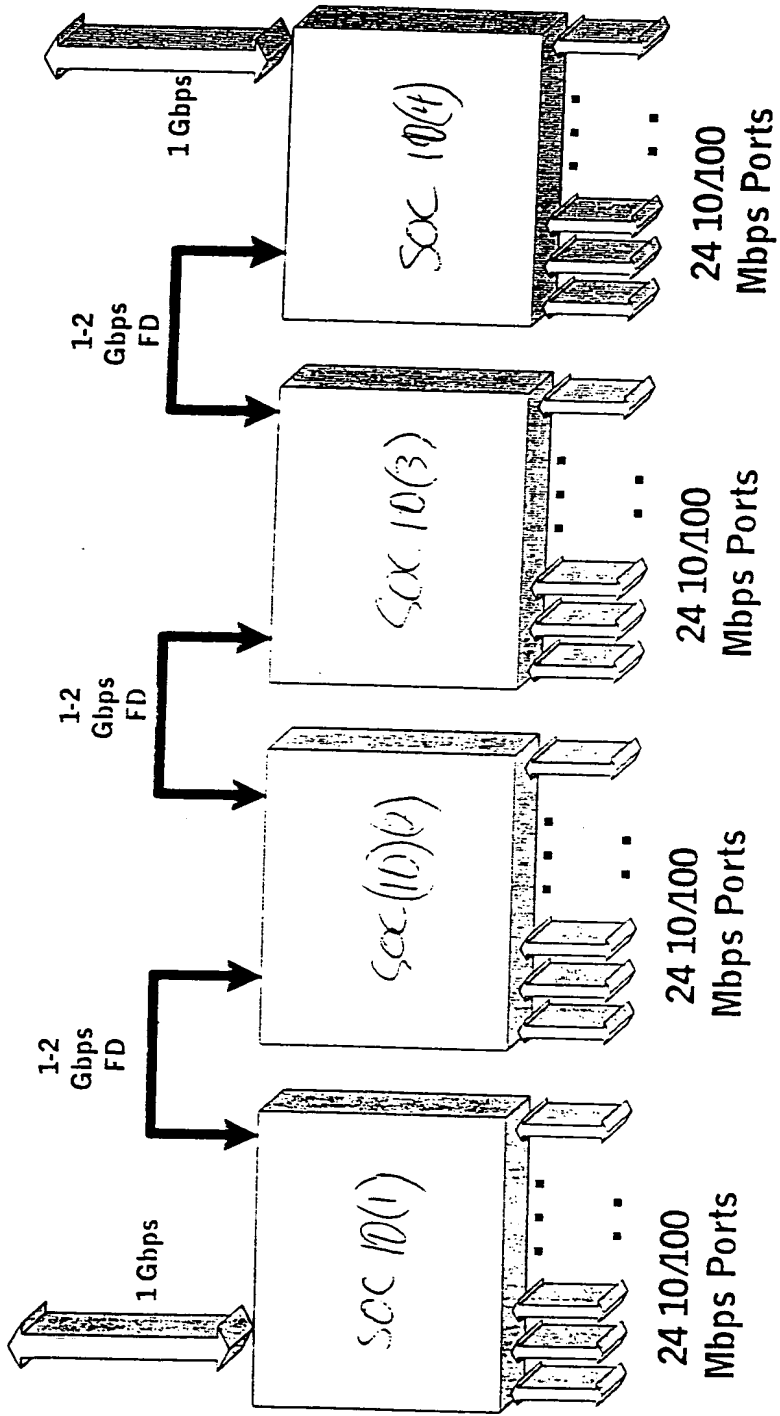


Fig. 22

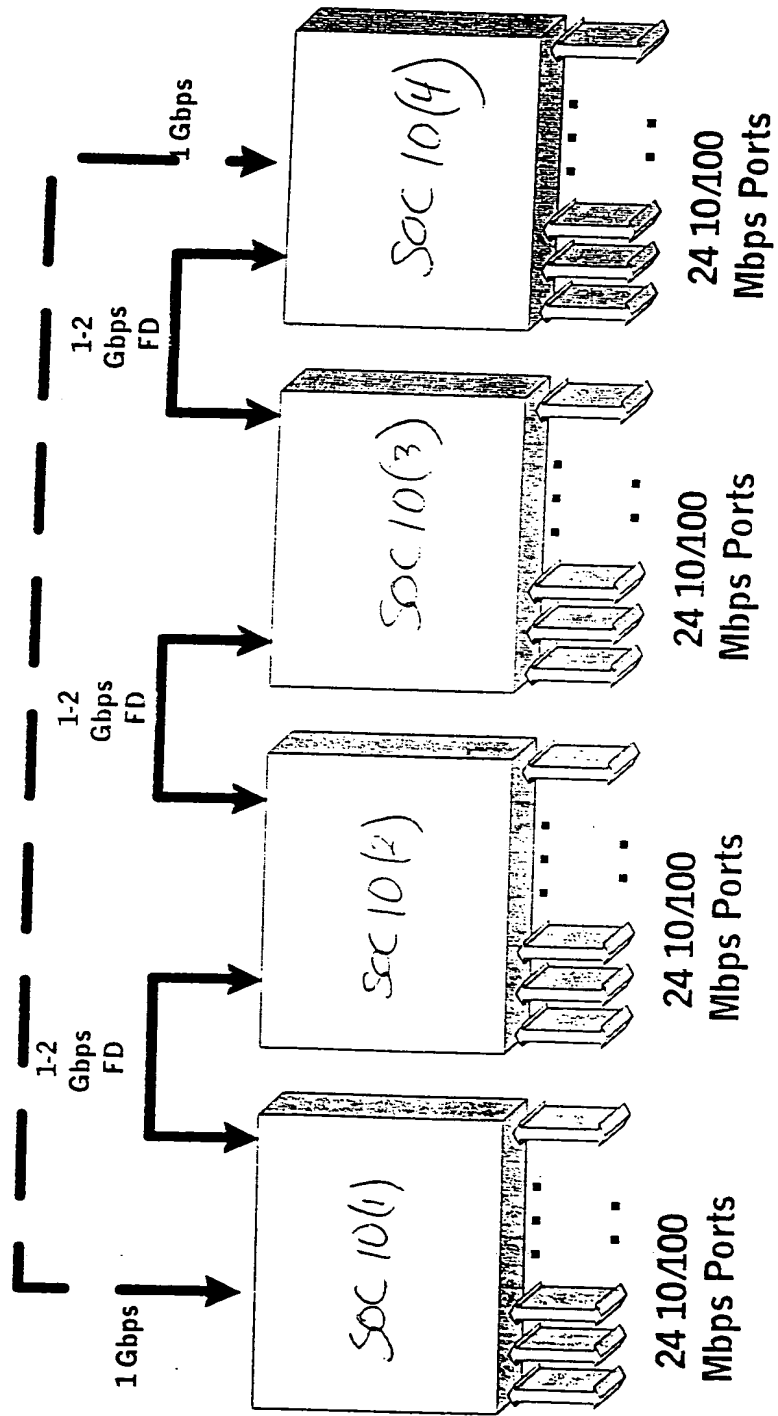


Fig. 23

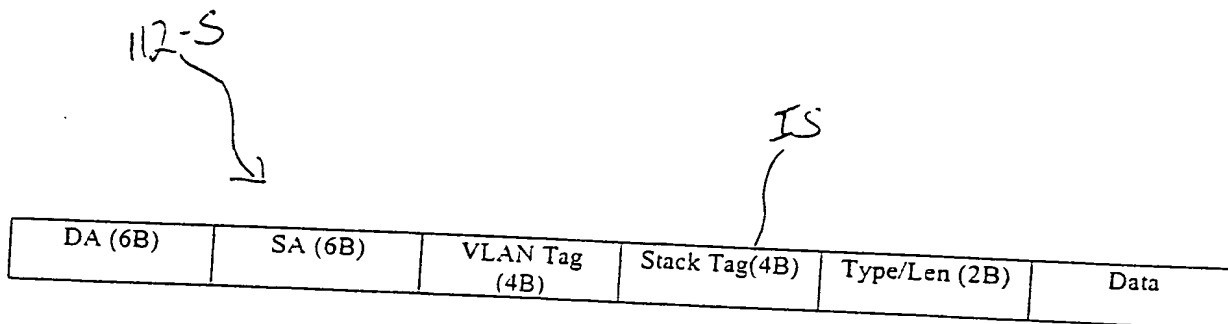


Fig. 24A

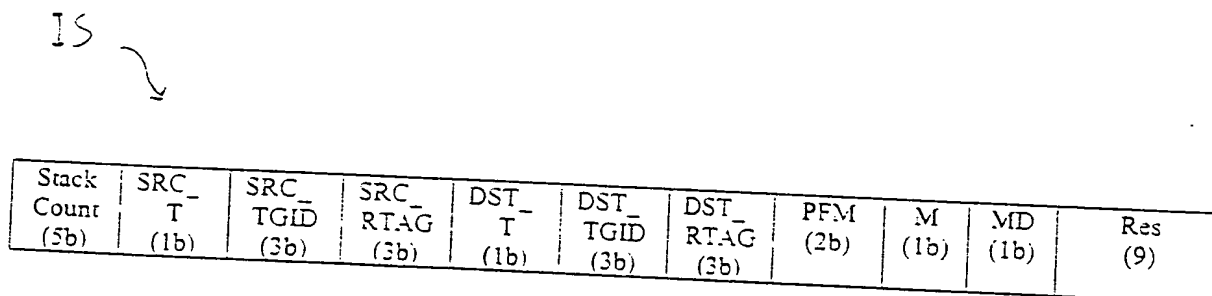


Fig. 24B

ה'תשס"ח

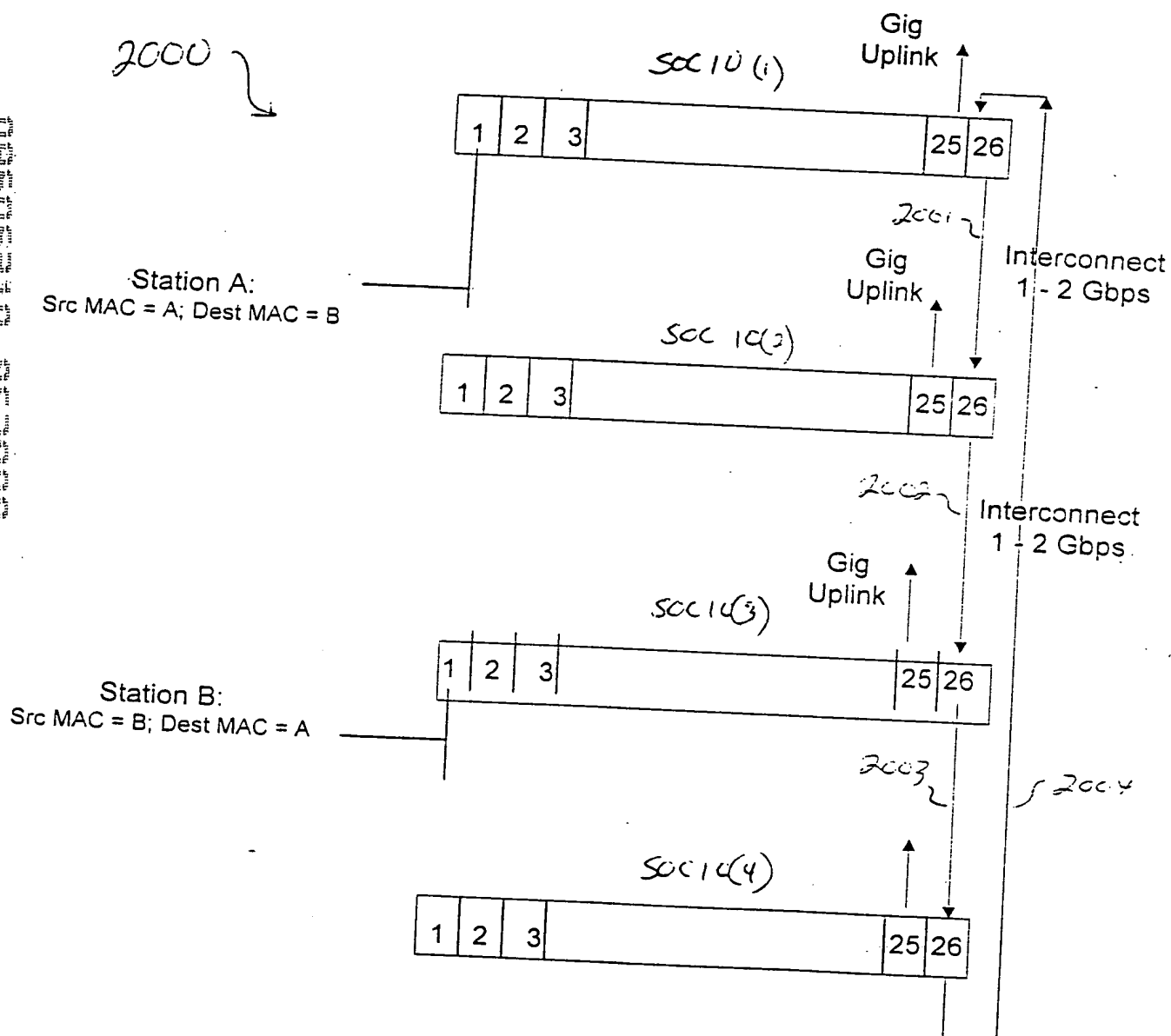


Fig 25

09606210-063900

2000

Station A:
Src MAC = A; Dest MAC = B

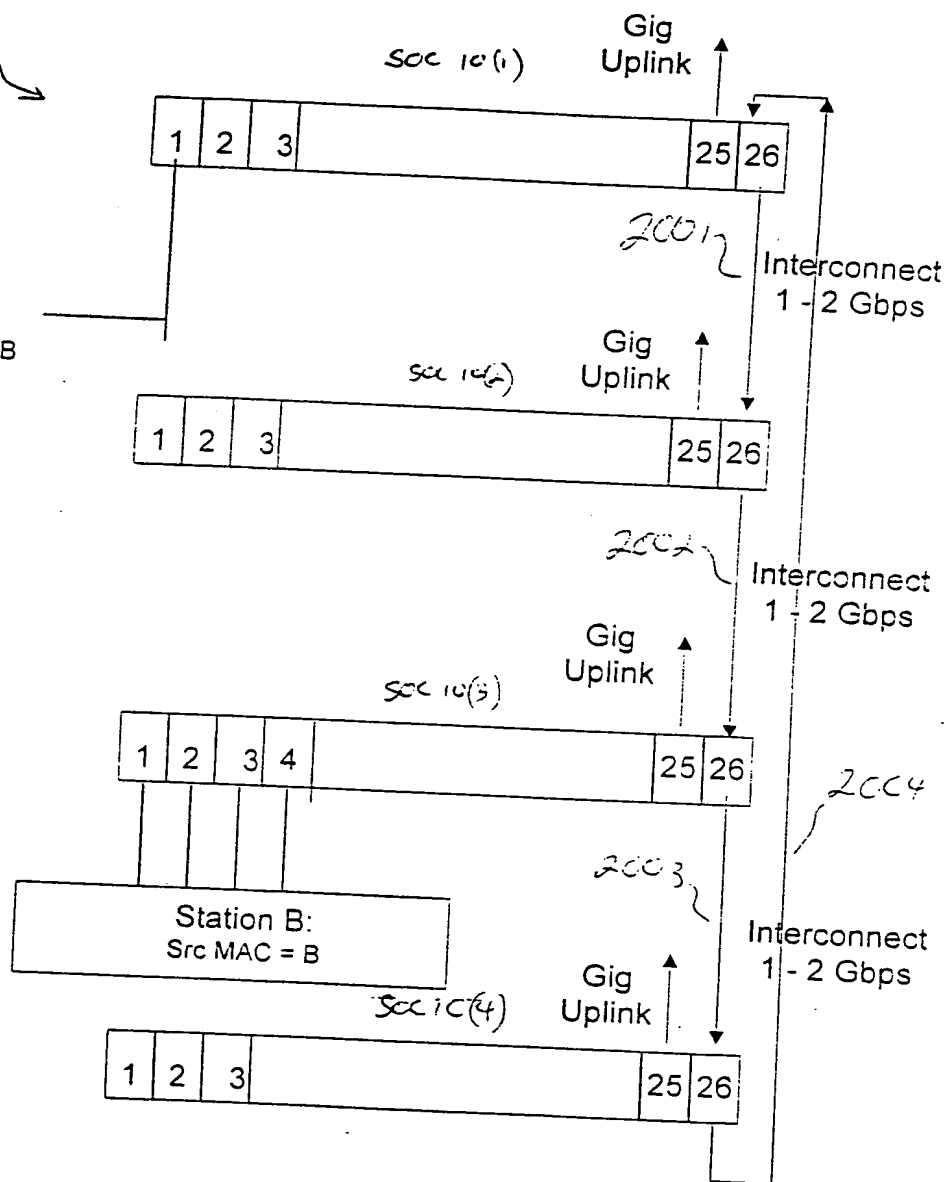


Fig. 26

Fig. 27A

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 1 | A | 1 | 0 | X | X |
| 26 | B | 1 | 1 | 2 | 2 |

Fig. 27B

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 26 | A | 1 | 0 | X | X |
| 26 | B | 1 | 1 | 2 | 2 |

Fig. 27C

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 26 | A | 1 | 0 | X | X |
| 1 | B | 1 | 1 | 2 | 2 |

Fig. 27D

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 26 | A | 1 | 0 | X | X |
| 26 | B | 1 | 1 | 2 | 2 |

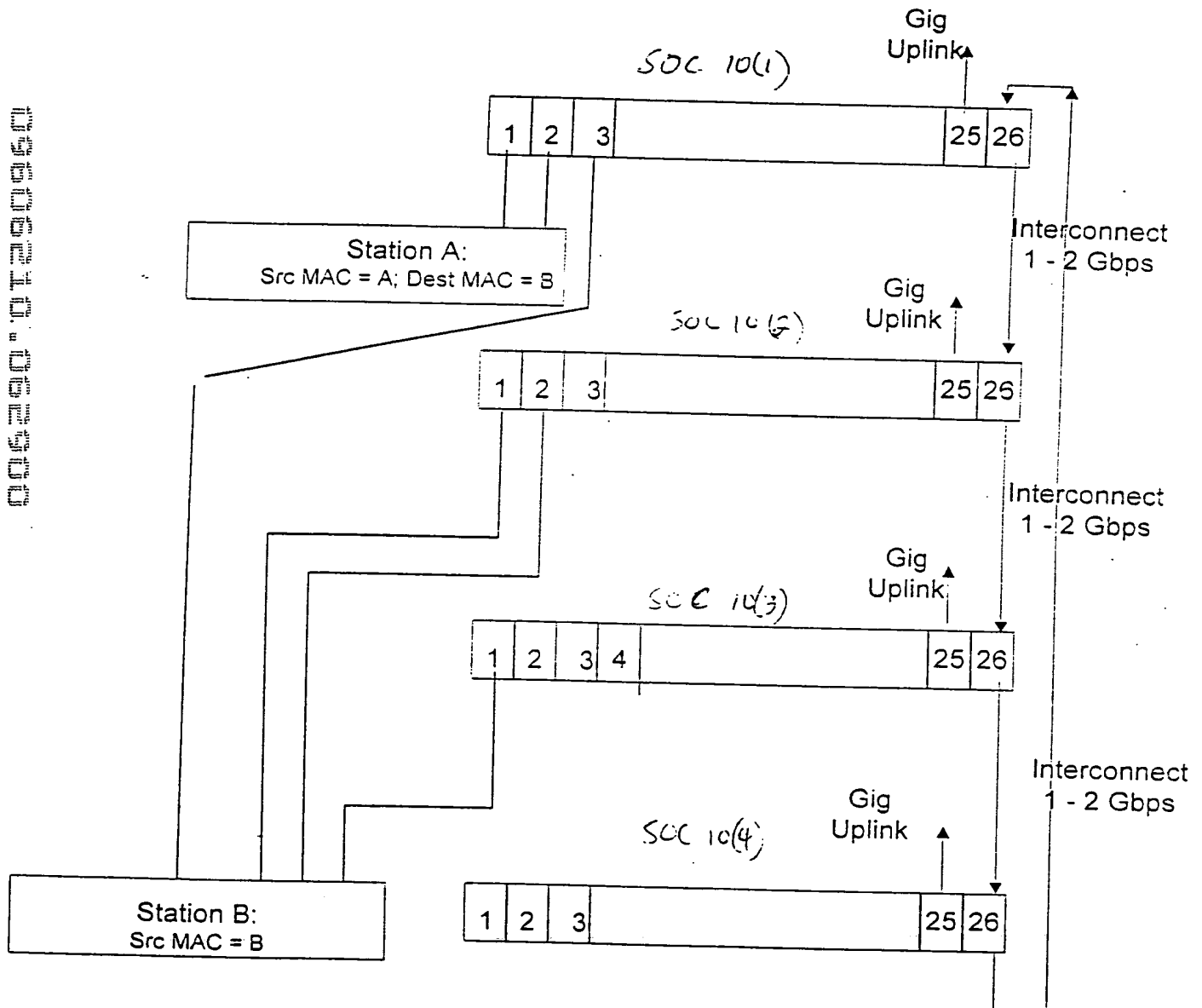


Fig. 8

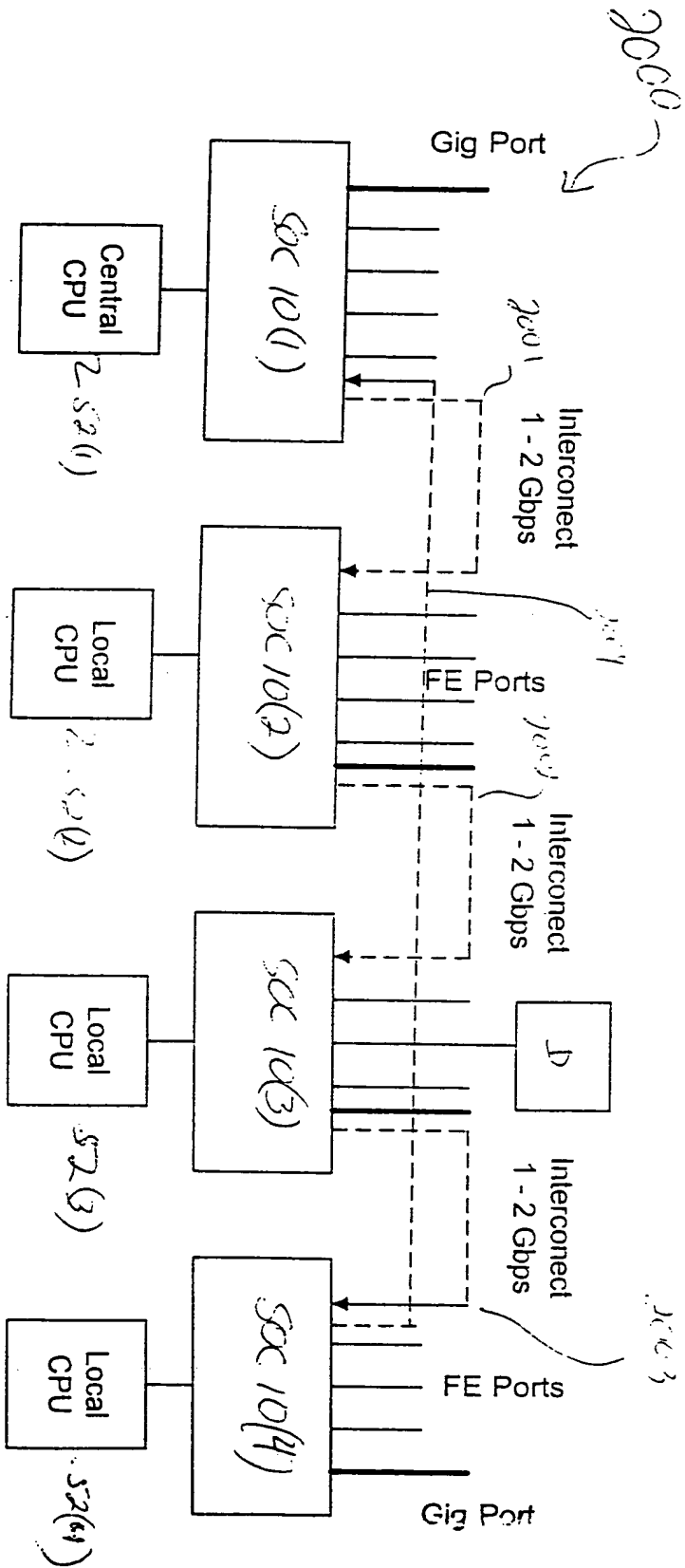


Fig. 29

09606210.0623100

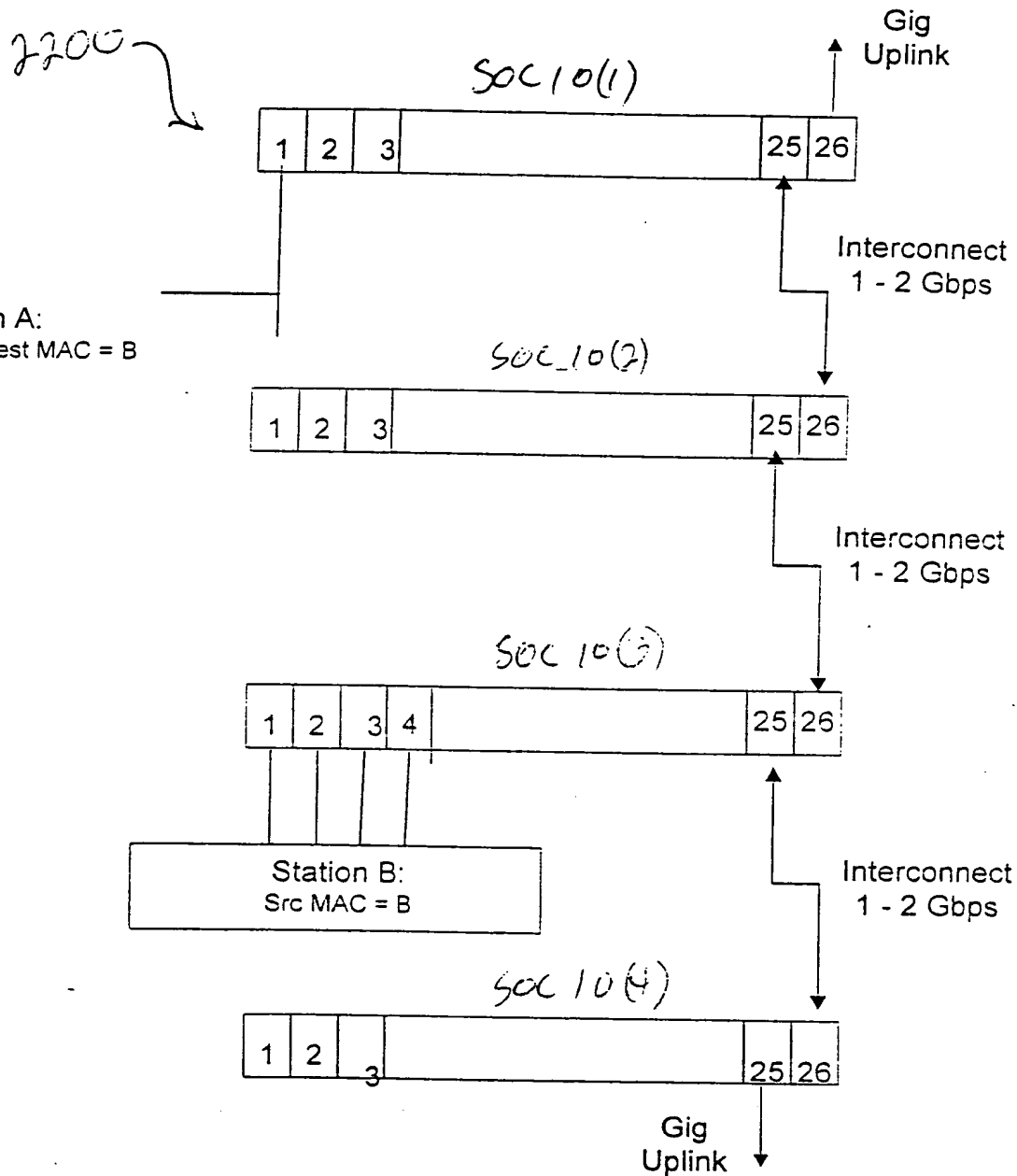


Fig 31

1. **Содержание:** 1. Введение. 2. Описание системы. 3. Структура системы. 4. Описание компонентов. 5. Описание работы системы. 6. Заключение.

F, 32B

F; 32 C

F 32D

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 26 | A | 1 | 0 | X | X |
| | | | | | |

THE

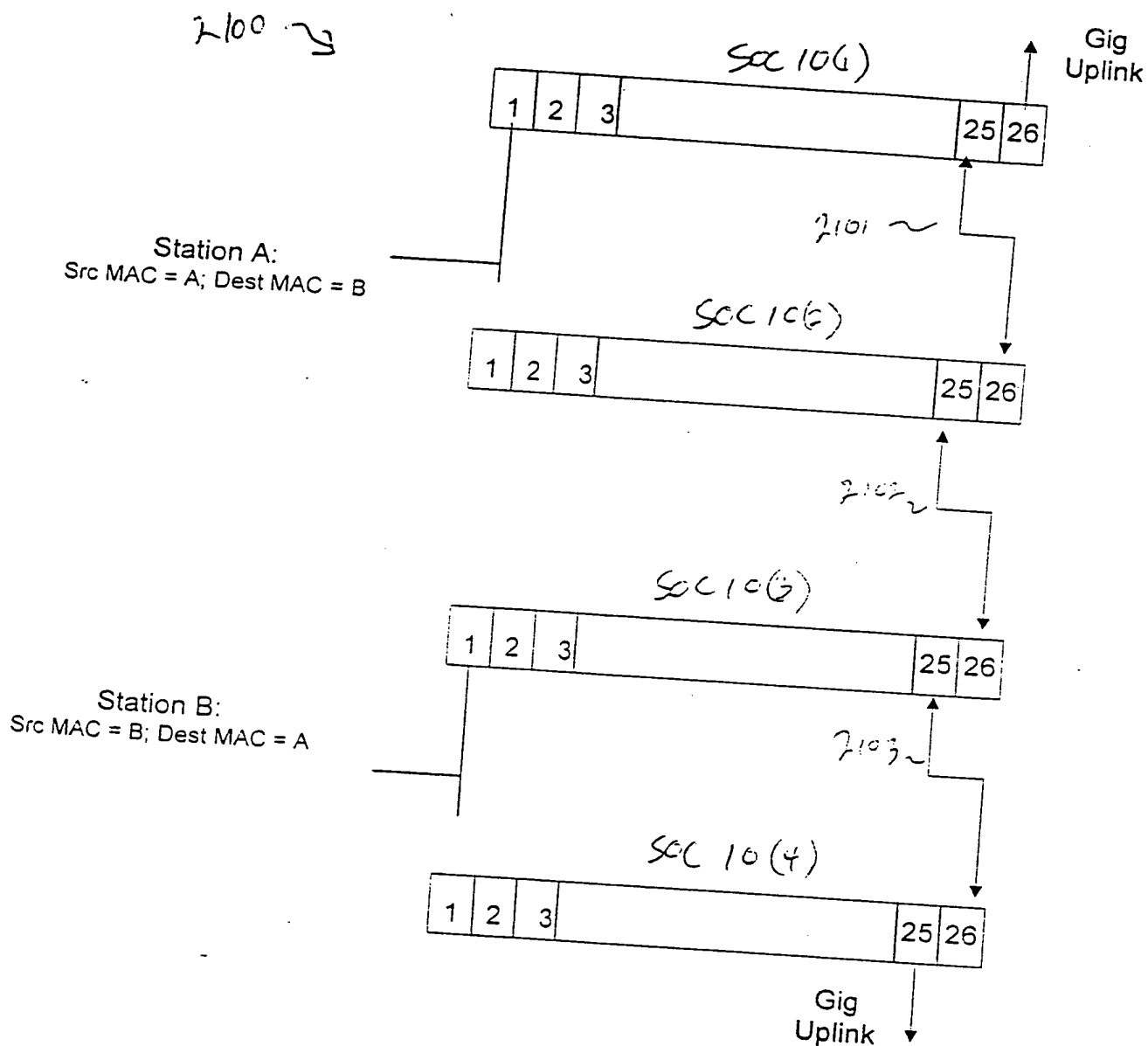


Fig 30.

ה'תשס"ח

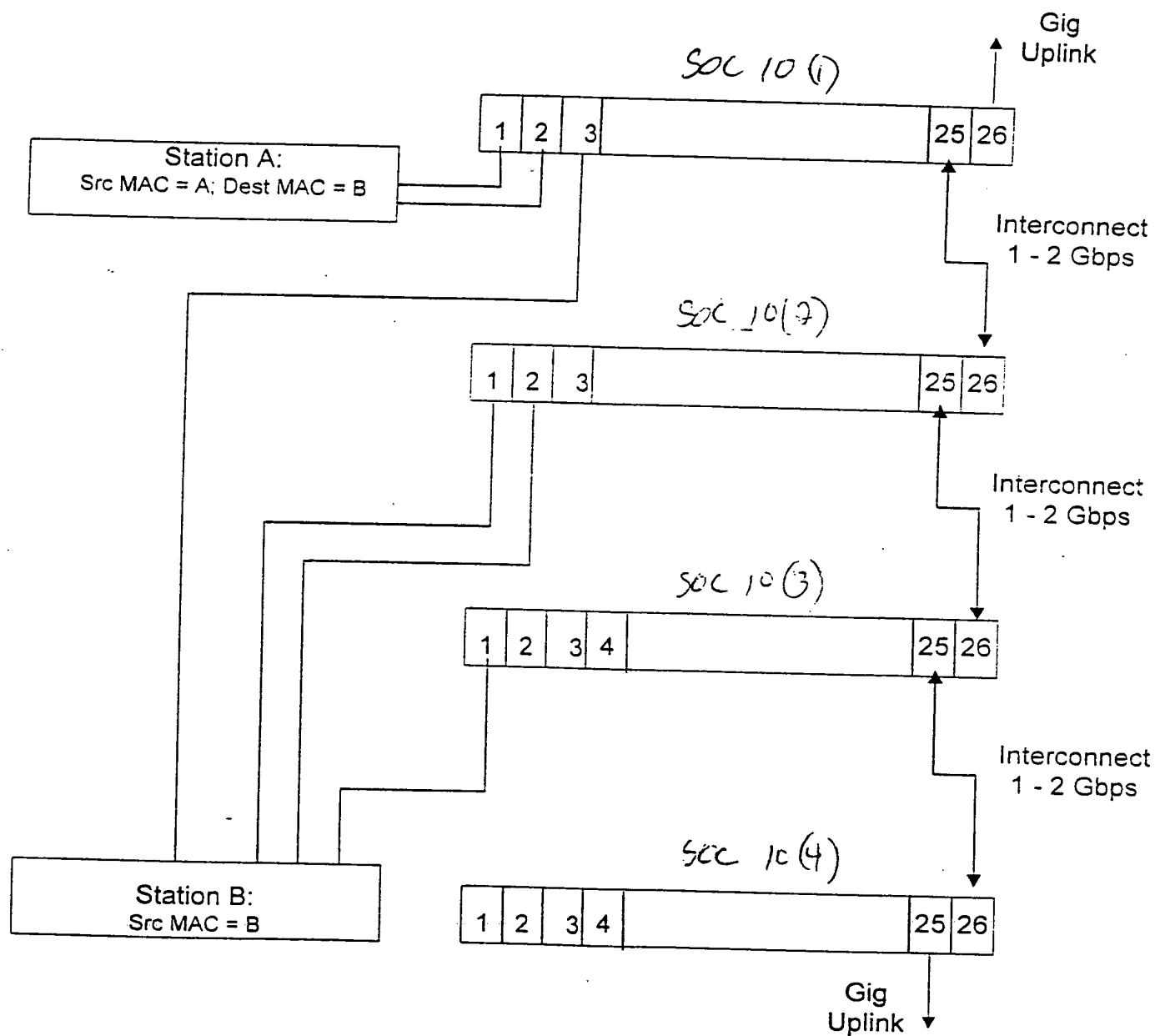


Fig 33

Fig. 34A

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 1 | A | 1 | 1 | 1 | 1 |
| 25 | B | 1 | 1 | 2 | 2 |

FS-34B

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 26 | A | 1 | 1 | 1 | 1 |
| 25 | B | 1 | 1 | 2 | 2 |

F.D. 34c

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 26 | A | 1 | 1 | 1 | 1 |
| 1 | B | 1 | 1 | 2 | 2 |

Fig. 34D

| Port Number | Mac Address | Vlan ID | T | TGID | RTAG |
|-------------|-------------|---------|---|------|------|
| 26 | A | 1 | 1 | 1 | 1 |
| | | | | | |

VLAN Id = 1

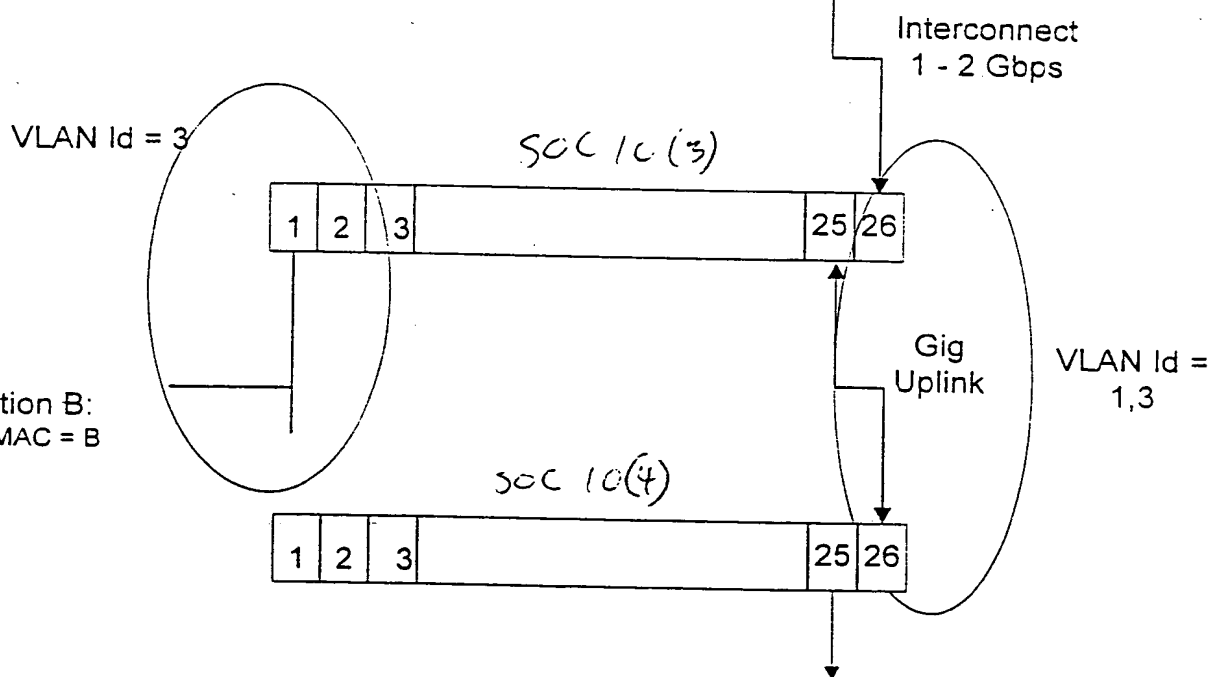
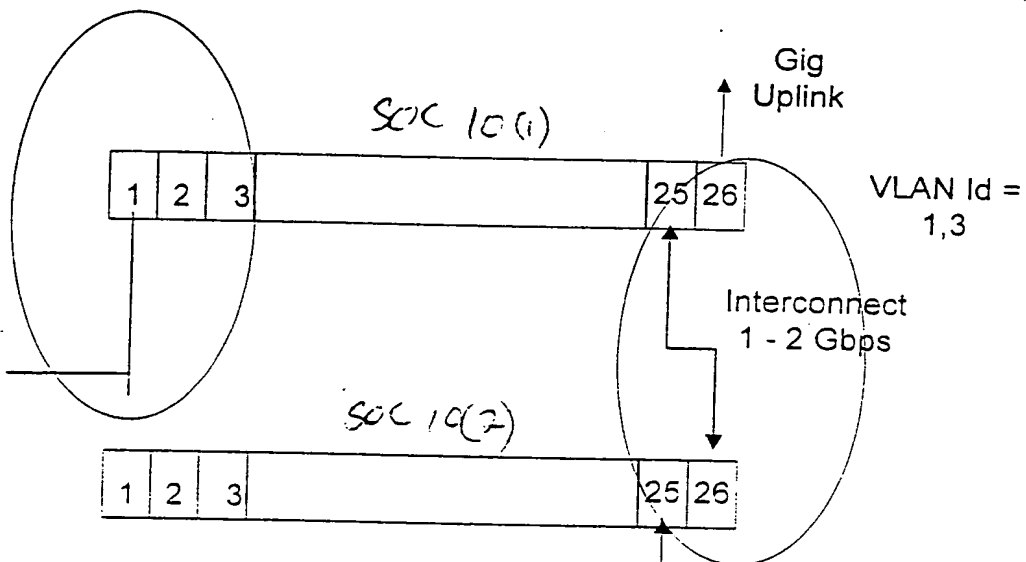


Fig. 35

Trunk Group Table for SW1:

| TGID | TP0 | TP1 | TP2 | TP3 | TP4 | TP5 | TP6 | TP7 | TG Size |
|------|-----|-----|-----|-----|-----|-----|-----|-----|---------|
| 2 | 25 | 25 | 25 | 25 | X | X | X | X | 4 |

Trunk Group Table for SW2:

| TGID | TP0 | TP1 | TP2 | TP3 | TP4 | TP5 | TP6 | TP7 | TG Size |
|------|-----|-----|-----|-----|-----|-----|-----|-----|---------|
| 2 | 25 | 25 | 25 | 25 | X | X | X | X | 4 |

Trunk Group Table for SW3:

| TGID | TP0 | TP1 | TP2 | TP3 | TP4 | TP5 | TP6 | TP7 | TG Size |
|------|-----|-----|-----|-----|-----|-----|-----|-----|---------|
| 2 | 1 | 2 | 3 | 4 | X | X | X | X | 4 |

Trunk Group Table for SW4:

| TGID | TP0 | TP1 | TP2 | TP3 | TP4 | TP5 | TP6 | TP7 | TG Size |
|------|-----|-----|-----|-----|-----|-----|-----|-----|---------|
| 2 | 26 | 26 | 26 | 26 | X | X | X | X | 4 |

Fig. 36

Trunk Group Table for SW1:

| TGID | TP0 | TP1 | TP2 | TP3 | TP4 | TP5 | TP6 | TP7 | TG Size |
|------|-----|-----|-----|-----|-----|-----|-----|-----|---------|
| 1 | 1 | 2 | X | X | X | X | X | X | 2 |
| 2 | 25 | 25 | 25 | 3 | X | X | X | X | 4 |

Trunk Group Table for SW2:

| TGID | TP0 | TP1 | TP2 | TP3 | TP4 | TP5 | TP6 | TP7 | TG Size |
|------|-----|-----|-----|-----|-----|-----|-----|-----|---------|
| 1 | 26 | 26 | X | X | X | X | X | X | 2 |
| 2 | 25 | 1 | 2 | 26 | X | X | X | X | 4 |

Trunk Group Table for SWS:

| TGID | TP0 | TP1 | TP2 | TP3 | TP4 | TP5 | TP6 | TP7 | TG Size |
|------|-----|-----|-----|-----|-----|-----|-----|-----|---------|
| 1 | 26 | 26 | X | X | X | X | X | X | 2 |
| 2 | 1 | 26 | 26 | 26 | X | X | X | X | 4 |

Trunk Group Table for SW4:

| TGID | TP0 | TP1 | TP2 | TP3 | TP4 | TP5 | TP6 | TP7 | TG Size |
|------|-----|-----|-----|-----|-----|-----|-----|-----|---------|
| 1 | 26 | 26 | X | X | X | X | X | X | 2 |
| 2 | 26 | 26 | 26 | 26 | X | X | X | X | 4 |

Fig. 37

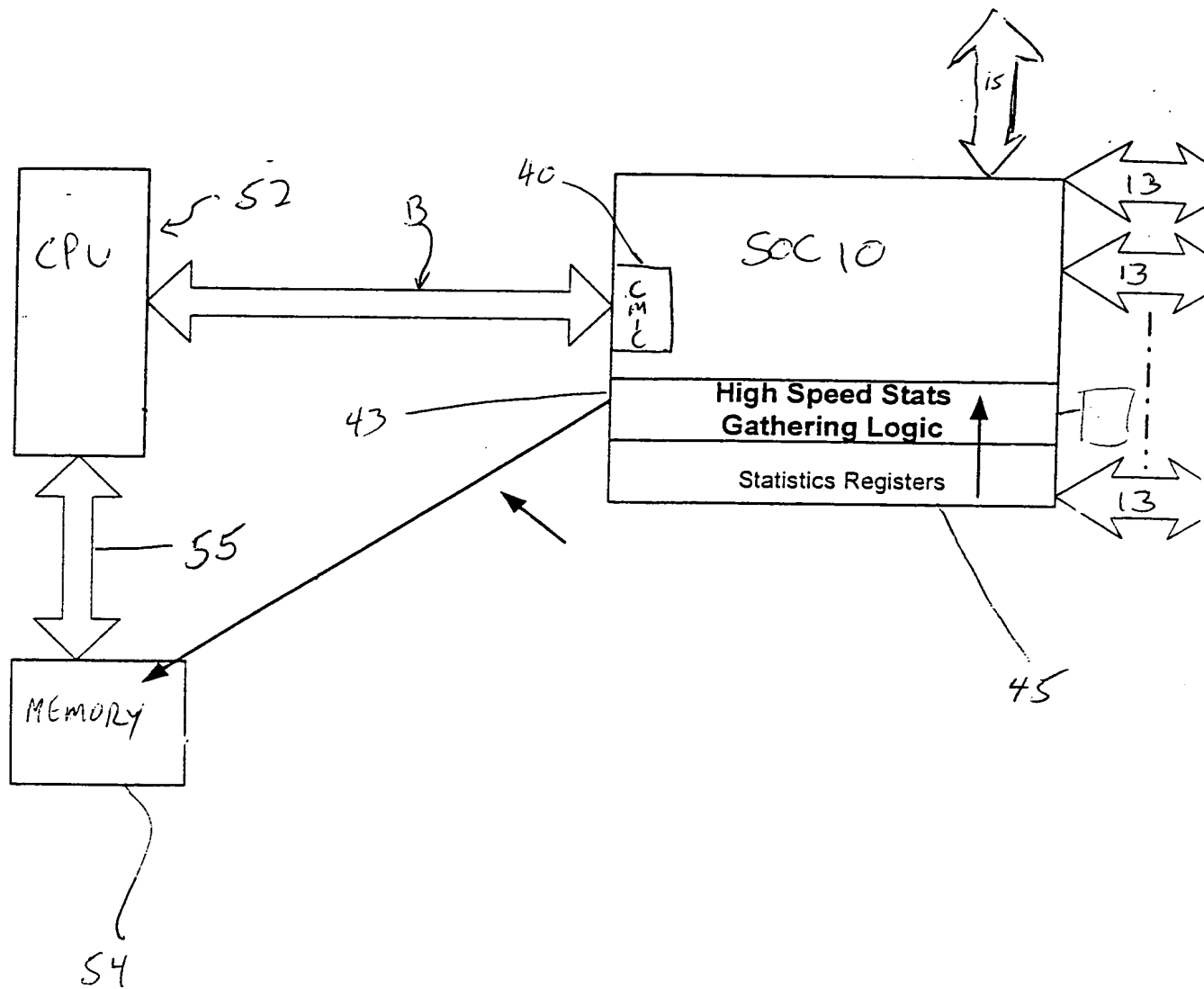


Fig. 38

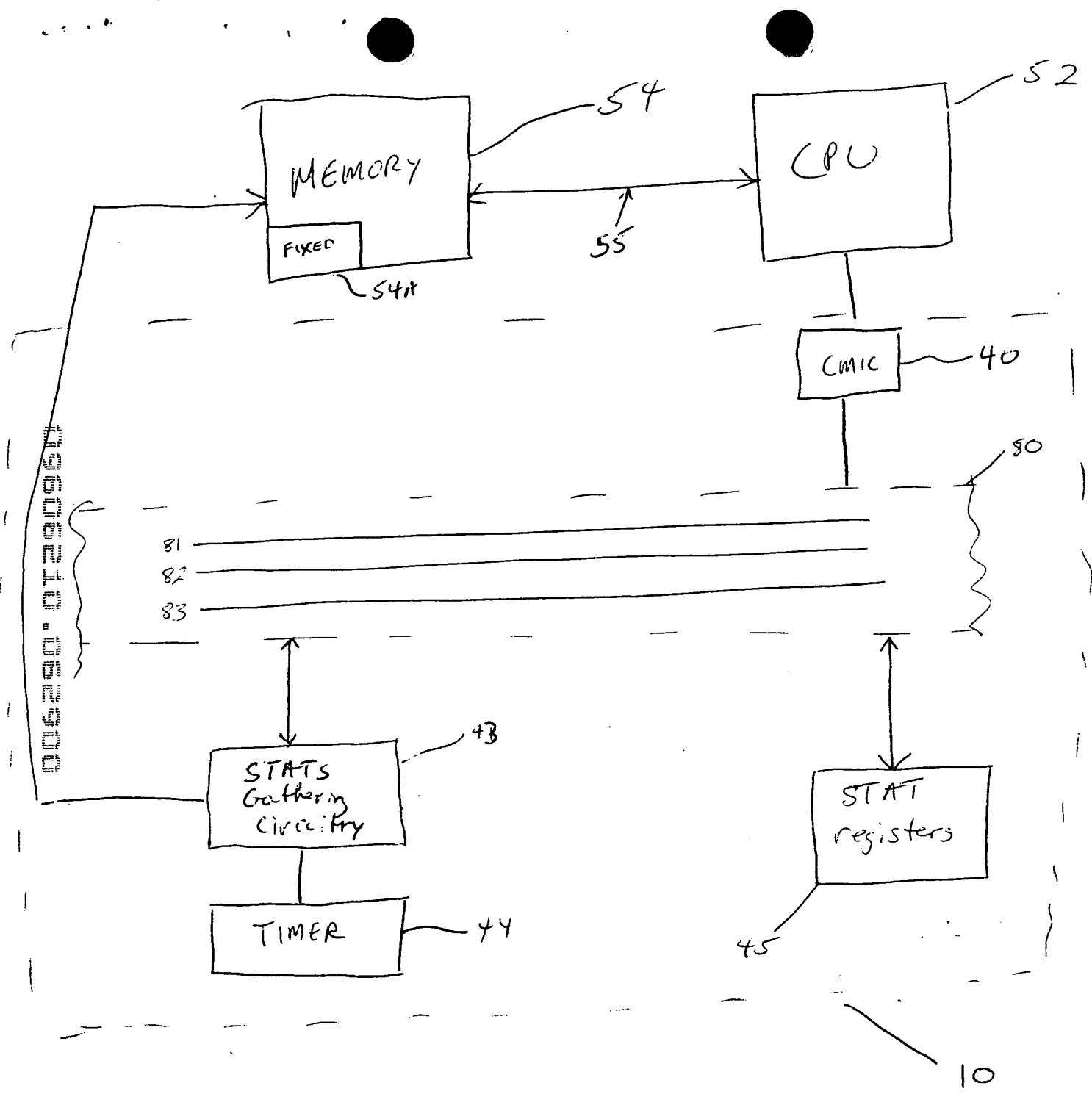


Fig. 39